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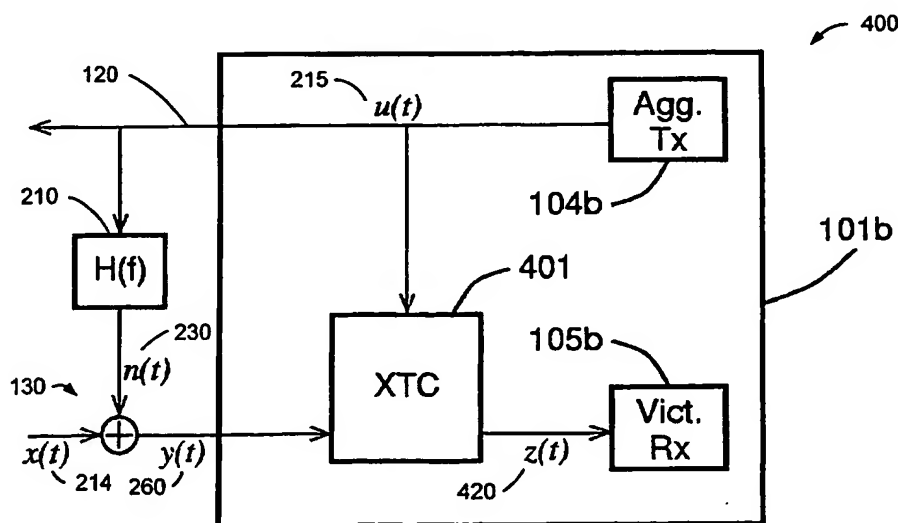
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(54) Title: METHOD AND SYSTEM FOR CROSSTALK CANCELLATION



(57) Abstract: Signals propagating in one communication channel can generate crosstalk interference in another communication channel. A crosstalk cancellation device can process the signals causing the crosstalk interference and generate a crosstalk cancellation signal that can compensate for the crosstalk when applied to the channel receiving crosstalk interference. The crosstalk cancellation device can include a model of the crosstalk effect that generates a signal emulating the actual crosstalk both in form and in timing. The crosstalk cancellation device can include a controller that monitors crosstalk-compensated communication signals and adjusts the model to enhance crosstalk cancellation performance. The crosstalk cancellation device can have a mode of self configuration or calibration in which defined test signals can be transmitted on the crosstalk-generating channel and the crosstalk-receiving channel.



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METHOD AND SYSTEM FOR CROSSTALK CANCELLATION

CROSS REFERENCE TO RELATED APPLICATIONS

This application claims the benefit of priority to U.S. Provisional Patent
5 Application Serial Number 60/494,072, entitled "Method for Crosstalk Cancellation in
High-Speed Communication Systems," and filed August 7, 2003. The contents of U.S.
Provisional Patent Application Serial Number 60/494,072 are hereby incorporated by
reference.

This application is related to U.S. Nonprovisional Patent Application Serial No.
10 10/108,598, entitled "Method and System for Decoding Multilevel Signals," filed on
March 28, 2002, and U.S. Nonprovisional Patent Application Serial No. 10/620,477,
entitled "Adaptive Noise Filtering and Equalization for Optimal High Speed Multilevel
Signal Decoding," filed on July 15, 2003. The contents of U.S. Patent Application Serial
No. 10/108,598 and U.S. Patent Application Serial No. 10/620,477 are hereby
15 incorporated by reference.

FIELD OF THE INVENTION

The present invention relates to the field of communications, and more
specifically to improving the signal fidelity in a communication system by compensating
20 for crosstalk interference that occurs between two or more communication channels that
is prevalent at high data communication rates.

BACKGROUND

Increased consumption of communication services fuels a need for increased data
25 carrying capacity or bandwidth in communication systems. A phenomenon known as
crosstalk often occurs in these communication systems and can impair high-speed signal
transmission and thus limit communication bandwidth to an undesirably low level.

Crosstalk is a condition that arises in communications systems wherein a signal in
one communication channel is corrupted by interference (or bleed-over) from a different
30 signal being communicated over another channel. The interference may arise due to a

variety of effects. For example, in electrical systems such as circuit boards, electrical connectors, and twisted pair cable bundles, each electrical path serves as a channel. At high communication speeds, these conductive paths behave like antennae, both radiating and receiving electromagnetic energy. The radiated energy from one channel, referred to herein as the “aggressing channel,” is undesirably coupled into or received by another channel, referred to herein as the “victim channel.” This undesirable transfer of signal energy, known as “crosstalk,” can compromise data integrity on the receiving channel. Crosstalk is typically bidirectional in that a single channel can both radiate energy to one or more other channels and receive energy from one or more other channels.

Crosstalk is emerging as a significant barrier to increasing throughput rates of communications systems. When not specifically addressed, crosstalk often manifests itself as noise. In particular, crosstalk degrades signal quality by increasing uncertainty in the received signal value thereby making reliable communications more difficult, i.e. data errors occur with increased probability. In other words, crosstalk typically becomes more problematic at increased data rates. Not only does crosstalk reduce signal integrity, but additionally, the amount of crosstalk often increases with the bandwidth of the aggressing signal, thereby making higher data rate communications more difficult. This is particularly the case in electrical systems employing binary or multi-level signaling, since the conductive paths over which such signals flow radiate and receive energy more efficiently at the high frequencies associated with the level transitions in these signals. In other words, each signal in a binary or multi-level communication signal is composed of high-frequency signal components that are more susceptible to crosstalk degradation compared to lower frequency components.

The crosstalk impediment to increasing data throughput rates is further compounded by the tendency of the high-frequency content of the victim signal to attenuate heavily over long signal transmission path lengths (e.g. circuit traces that are several inches in length for multi-gigabit per second data rates). That is, high-frequency components of a communication signal not only receive a relatively high level of crosstalk interference, but also are susceptible to interference because they are often weak due to transmission losses.

While these attenuated high-frequency components can be amplified via a technique known as channel equalization, such channel equalization frequently increases noise and crosstalk as a byproduct of amplifying the high-frequency signals that carry data. The amount of crosstalk present in a communication link often limits the level of equalization that can be utilized to restore signal integrity. For example, at the multi-gigabit per second data rates desired for next-generation backplane systems, the level of crosstalk energy on a communication channel can exceed the level of victim signal energy at the high frequencies that underlie such high-speed communication. In this condition, extraneous or stray signal energy can dominate the energy of the desirable data-carrying signals, thus rendering communicating at these data rates impractical with most conventional system architectures.

The term "noise," as used herein, is distinct from crosstalk and refers to a completely random phenomenon. Crosstalk, in contrast, is a deterministic, but often unknown, parameter. The conventional art includes knowledge that it is theoretically possible to modify a system in order to mitigate crosstalk. In particular, with definitions of: (i) the data communicated over an interfering or aggressing channel; and (ii) the signal transformation that occurs in coupling from the aggressing channel to the victim channel, the crosstalk can be theoretically determined and cancelled. That is, those skilled in the art understand that crosstalk signal degradation can be cancelled if the data carried by a communication signal that is input into a communication channel is known and the signal transformation imposed on the communication signal by crosstalk is also known. However, achieving a level of definition of this signal transformation having sufficient precision and accuracy to support a practical implementation of a system that adequately cancels crosstalk is difficult with conventional technology. Consequently, conventional technology that addresses crosstalk is generally insufficient for high-speed (e.g. multi-gigabit per second) communications systems. Thus, there is a need in the art to cancel crosstalk so as to improve victim signal fidelity and remove the barrier that crosstalk often poses to increasing data throughput rates.

While the physics giving rise to crosstalk (e.g. electromagnetic coupling in electrical systems or four-wave-mixing in optical systems) is well understood, this

understanding alone does not provide direct and simple models for the crosstalk transfer function. One common reason for conventional modeling difficulties is that the relative geometries of the victim and aggressor signal paths heavily influence the transfer function of the crosstalk effect, and these paths can be quite convoluted. In other words, signal path complexity typically checks efforts to model crosstalk using conventional modeling methods based on analyzing signal conduits. Furthermore, it is generally undesirable to design a crosstalk canceller for a predetermined specific crosstalk response since: (i) a system may have many different responses for different victim-aggressor pairs (each requiring a specific design); and (ii) different systems may need different sets of designs. Thus, there is a need in the art for a crosstalk cancellation system and method with sufficient flexibility to: (i) accommodate the variety of crosstalk transfer functions that can stem from ordinary operations of a given system; and (ii) self-calibrate in order to avoid a complex manual task of characterizing and adjusting for each victim-aggressor pair.

While the general concept of crosstalk cancellation is known in the conventional art, conventional crosstalk cancellation is typically not applicable to high-speed environments, such as channels supporting multi-gigabaud rates. Conventional crosstalk cancellation is typically implemented in an entirely digital environment, wherein the accessible aggressing data signals and received victim signals are digitized, and a microprocessor implements the cancellation processes. The analog-to-digital converters and microprocessors needed to implement this digital crosstalk cancellation in a high-speed environment are usually excessively complex, resulting in unacceptable power consumption and product cost.

To address these representative deficiencies in the art, what is needed is a capability for crosstalk cancellation compatible with high-speed environments but with low power consumption and reasonable production cost. A capability is further needed for automatically calibrating or configuring crosstalk cancellation devices. Such capabilities would facilitate higher data rates and improve bandwidth in diverse communication applications.

SUMMARY OF THE INVENTION

The present invention supports compensating for signal interference, such as crosstalk, occurring between two or more communication channels. Compensating for crosstalk can improve signal quality and enhance communication bandwidth or information carrying capability.

A communication signal transmitted on one communication channel can couple an unwanted signal, such as crosstalk, into another communication channel and interfere with communication signals transmitting on that channel. In addition to occurring between two channels, this crosstalk effect can couple between and among multiple communication channels with each channel imposing crosstalk on two or more channels and receiving crosstalk from two or more channels. A channel can be a medium, such as an electrical conductor or an optical fiber that provides a signal path. A single optical fiber or wire can provide a transmission medium for two or more channels, each communicating digital or analog information. Alternatively, each channel can have a dedicated transmission medium. For example, a circuit board can have multiple conductors in the form of circuit traces in which each trace provides a dedicated communication channel.

In another aspect of the present invention, a crosstalk cancellation device can input a crosstalk cancellation signal into a channel receiving crosstalk interference to cancel or otherwise compensate for the received crosstalk. The crosstalk cancellation signal can be derived or produced from a signal that is propagating on another channel, generating the crosstalk. The crosstalk cancellation device can be coupled between the channel that generates the crosstalk and the channel that receives the crosstalk. In this configuration, the crosstalk cancellation device can sample or receive a portion of the signal that is causing the crosstalk and can compose the crosstalk cancellation signal for application to the channel that is receiving the unwanted crosstalk. In other words, the crosstalk cancellation device can tap into the channel that is causing the crosstalk, generate a crosstalk cancellation signal, and apply the crosstalk cancellation signal to the channel receiving crosstalk interference to provide crosstalk cancellation or correction.

In another aspect of the present invention, the crosstalk cancellation device can generate the crosstalk cancellation signal via a model of the crosstalk effect. The model can generate the crosstalk cancellation signal in the form of a signal that estimates, approximates, emulates, or resembles the crosstalk signal. The crosstalk cancellation
5 signal can have a waveform or shape that matches the actual crosstalk signal. A setting or adjustment that adjusts the model, such as a set of modeling parameters, can define characteristics of this waveform.

The crosstalk cancellation signal can be synchronized with the actual crosstalk signal. That is, the timing of the crosstalk cancellation signal can be adjusted to match
10 the timing of the actual crosstalk signal. A timing delay or other timing parameter can define the relative timing or temporal correspondence between the crosstalk cancellation signal and the actual crosstalk signal.

In another aspect of the present invention, the crosstalk cancellation device can implement modeling and timing adjustments so the crosstalk cancellation signal closely
15 matches the actual crosstalk, thereby yielding effective crosstalk cancellation. A controller of the crosstalk cancellation device can monitor and analyze the output of the crosstalk cancellation device. That is, a controller can process the crosstalk-cancelled signal, which is an improved communication signal that results from applying the crosstalk cancellation signal to the channel having crosstalk interference. The controller
20 can vary the modeling parameters and the timing delay, individually or in unison, to minimize any residual crosstalk remaining after crosstalk cancellation. Adjusting the operations of the crosstalk cancellation device can compensate for fluctuating conditions and variations in the crosstalk effect.

In another aspect of the present invention, a crosstalk cancellation device can
25 undergo a calibration or setup procedure that is initiated internally or externally. The crosstalk cancellation device, or another device executing the calibration procedure, can initiate the transmission of a known or predetermined test signal on a communication channel. A test signal can be transmitted on the channel that causes crosstalk or the channel that receives crosstalk interference. Also, one test signal can be transmitted on a
30 channel generating crosstalk, while a different test signal is transmitted on a channel that

receives the generated crosstalk interference. For example, a randomized communication signal can propagate on the crosstalk-generating channel, while the crosstalk-receiving channel can have a uniform voltage or current signal that is representative of essentially no data transmission. The crosstalk cancellation device can utilize these known
5 conditions to define the timing and shape of a crosstalk cancellation signal that effectively compensates for crosstalk interference. In other words, the crosstalk cancellation device can define or refine its model of the crosstalk effect based on operating the crosstalk cancellation device with test signals transmitting on the crosstalk-generating and the crosstalk-receiving communication channels.

10 The discussion of correcting crosstalk presented in this summary is for illustrative purposes only. Various aspects of the present invention may be more clearly understood and appreciated from a review of the following detailed description of the disclosed embodiments and by reference to the drawings and claims.

15 BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 illustrates a functional block diagram of a communication system having two linecards communicating over a backplane and incurring crosstalk.

Figure 2 illustrates a functional block diagram of a crosstalk model for the system illustrated in Figure 1.

20 Figure 3 illustrates a plot of a crosstalk response for a backplane-to-linecard connector according to an exemplary embodiment of the present invention.

Figure 4 illustrates a functional block diagram of a crosstalk cancellation system according to an exemplary embodiment of the present invention.

25 Figure 5 illustrates a functional block diagram of a crosstalk cancellation system including functional blocks of a crosstalk cancellation device according to an exemplary embodiment of the present invention.

Figure 6 is a functional block diagram of a tapped delay line filter according to an exemplary embodiment of the present invention.

Figure 7 is a functional block diagram of a crosstalk modeling filter of a crosstalk cancellation device with an adjustable delay according to an exemplary embodiment of the present invention.

Figure 8 is a functional block diagram of a crosstalk modeling filter of a crosstalk cancellation device with a high-pass filter according to an exemplary embodiment of the present invention.

Figure 9 is a functional block diagram of a control module of a crosstalk cancellation device according to an exemplary embodiment of the present invention.

Figure 10 is a flow chart illustrating a process for canceling crosstalk according to an exemplary embodiment of the present invention.

Figure 11 is a flow chart illustrating a process for calibrating a crosstalk cancellation device according to an exemplary embodiment of the present invention.

Figures 12A and 12B respectively illustrate testing data of a communication system before and after implementing crosstalk cancellation according to an exemplary embodiment of the present invention.

DETAILED DESCRIPTION OF EXEMPLARY EMBODIMENTS

The present invention supports canceling crosstalk on one or more communication paths in a communication system, such as a high-speed digital data communication system. A flexible and adaptable model of the crosstalk effect can output a cancellation signal that accurately represents crosstalk interference. Coupling this cancellation signal onto a signal path that has crosstalk can cancel such crosstalk and thereby negate the impairment that crosstalk can impose on bandwidth.

Turning now to discuss each of the drawings presented in Figures 1-12B, in which like numerals indicate like elements throughout the several figures, an exemplary embodiment of the present invention will be described in detail.

Turning now to Figure 1, this figure illustrates a functional block diagram of a communication system 100 having two linecards 101a, 101b communicating over backplane signal paths 120, 130 exhibiting crosstalk 150, 151. More specifically, Figure

1 illustrates an occurrence of backplane crosstalk 150, and connector crosstalk 151 in the exemplary case of a backplane communications system 100.

A linecard 101a, 101b is a module, typically a circuit board that slides in and out of a chassis slot and provides communication capabilities associated with a communication channel. A backplane 103 is a set of signal paths, such as circuit traces, at the rear of such a chassis that transmit signals between each installed linecard 101a, 101b and another communication device, such as another linecard 101a, 101b or a data processing component in a rack-mounted digital communication system.

Each linecard 101a, 101b in the system 100 illustrated in Figure 1 transmits and receives multiple channels of data, such as the two illustrated channels 120, 130. An exemplary channel 130: (i) starts at a transmitter (Tx) 104a on a linecard 101a; (ii) transmits off the linecard 101a through a connector 102a to the backplane 103; (iii) continues across the backplane 103 to another connector 102b and linecard 101b; and (iv) is received by a receiver (Rx) 105b. Figure 1 shows two such channels labeled the “victim” or “vict.” (from victim transmitter 104a to victim receiver 105b) and the “aggressor” or “agg.” (from aggressor transmitter 104b to aggressor receiver 105a).

When the signal paths 120, 130 are in close proximity to one another, signal energy radiates from the aggressor channel 120 and is incorporated into the victim channel 130. That is, in areas of the backplane 103 and connectors 102a, 102b in which a first signal path is located close to a second signal path, a portion of the signal energy propagating in the first signal path can couple into the second signal path and corrupt or impair signals propagating on this second signal path. This crosstalk coupling 150 may occur on a linecard 101a, 101b, in a connector 102a, 102b, on a backplane 103, or any combination thereof, for example.

While not illustrated in Figure 1, crosstalk can also occur in the reverse direction. Specifically, the “victim” channel 130 often radiates energy which corrupts the “aggressor” channel 120. That is, crosstalk frequently occurs in a bidirectional manner, transferring not only from a first signal path to a second signal path but also from the second signal path to the first signal path. Furthermore, in systems having three or more signal paths coexisting in close proximity to one another (not illustrated), crosstalk can

transfer among and between three or more signal paths. That is, a single signal can not only impose crosstalk on two or more other signals, but also receive crosstalk interference from two or more other signals.

Similar to the multi-physical-path case illustrated in Figure 1 and described
5 above, crosstalk can occur with the aggressor and victim channels propagating on a single transmission medium (e.g. a single cable or trace). In this scenario, each channel can correspond to a particular signal band (e.g. a frequency band in frequency division multiplexing system, a spectral band as in an optical wavelength division multiplexing system, or a temporal window in time-division multiplexing system). In other words, two
10 communication channels, one generating crosstalk, one receiving crosstalk, can coexist in a communication medium such as an optical waveguide or a wire, with each communication channel supporting transmission of a dedicated communication signal.

For clarity of explanation, an exemplary embodiment of the present invention based on crosstalk occurring between two channels, each on a separate physical path, is
15 illustrated in Figure 1 and described in detail herein. In another exemplary embodiment of the present invention, a method and system cancels crosstalk occurring between channels coexisting on a single communication medium. One skilled in the art should be able to make and use the present invention in an application having two or more channels exhibiting crosstalk on a single communication medium following the detailed
20 description, flow charts, plots, and functional block diagrams included herein.

Turning now to Figure 2, this figure illustrates a functional block diagram 200 of a crosstalk model 210 for the system 100 illustrated in Figure 1. More specifically, Figure 2 illustrates a model 210 of the crosstalk effect 151 in the connector 102b based on a single, exemplary transfer function 210.

25 The aggressor transmitter 104b outputs an aggressor communication signal $u(t)$ 215 on the aggressor channel 120. Energy from this aggressor communication signal $u(t)$ 215 couples into the victim channel 130 via crosstalk 151 in the connector 102b. The aggressor communication signal $u(t)$ 215 is composed of a spread of frequencies. Since crosstalk 151 is a frequency dependent phenomenon, the frequencies of the
30 aggressor communication signal $u(t)$ 215 couple into the victim channel at varying

efficiency. The frequency model $H(f)$ 210 of the crosstalk effect 151 expresses the extent to which each of these frequency components couples into the victim channel 130 in the form of a signal $n(t)$ 230. This crosstalk signal $n(t)$ 230 combines with the unadulterated communication signal $x(t)$ 214 propagating on the victim channel 130 from the victim transmitter 104a. The victim channel 130 transmits the resulting combined signal $y(t)$ 260 to the victim receiver 105b.

The crosstalk transfer function 210 can be characterized by the frequency response $H(f)$ 210 (or its time-domain equivalent impulse response $h(t)$). As depicted in Figure 2, the response $H(f)$ 210 conveys the transformation that the aggressor data signal $u(t)$ 215 experiences in the connector portion of its route from the aggressing transmitter 104b to the victim receiver 105b. The specifics of this response 210 usually vary among particular victim-aggressor channel pairs. Nevertheless, the general nature of the response is based on geometric constraints and underlying physics. For example, a backplane connector's crosstalk response 151 can depend upon physical system parameters. The backplane crosstalk 150 can also be modeled with a transfer function, and the backplane and connector crosstalks 150, 151 can even be captured with a single (albeit different) transfer function.

An exemplary, non-limiting embodiment of the present invention, in which a crosstalk cancellation device compensates for crosstalk occurring on a linecard-to-backplane connection will be described below with reference to Figures 3-12B. The embodiments disclosed herein are provided so that this disclosure will be thorough and complete and will convey the scope of the invention to those having ordinary skill in the art. Those skilled in the art will appreciate that the present invention can be applied to address crosstalk occurring on a backplane or other locations in a communication system and that the present invention can compensate for various forms of crosstalk.

Turning now to Figure 3, this figure illustrates a plot 300 of a crosstalk response 210 for a backplane-to-linecard connector 102b according to an exemplary embodiment of the present invention. This plot 300 illustrates laboratory measurements of the power in the crosstalk signal 151, more specifically the power transferred from the aggressor channel 120 to the victim channel 130 in the connector 102b, as a function of

frequency. The horizontal axis is frequency measured in units of gigahertz (GHz). The vertical axis describes signal power in decibels ("dB"), more specifically ten times the base-ten logarithm of the crosstalk frequency response 210 squared. Thus, this plot 300 illustrates the level of crosstalk power transferred from one channel 120 to another channel 130 for each frequency component of the aggressor signal $u(t)$ 215.

In connectors 102a, 102b, the dominant mechanism for crosstalk 151 is typically capacitive coupling between the connector's pins. This mechanism is clearly evident in Figure 3 as the general high-pass nature of the response of the plot 300. In other words, the plot 300 shows a trend of higher signal frequencies, above about 1 GHz, transferring energy via crosstalk mechanisms 151 more readily than lower frequencies, below 1 GHz. The left side of the plot 300, less than approximately 1 GHz, exhibits an attenuated crosstalk signal having a power less than approximately -25 dB. Thus, this plot 300 shows that the frequency components less than approximately 1 GHz of a communication signal $u(t)$ 215 transfer a relatively small portion of their carried power to a victim channel 130 via connector crosstalk 151. The magnitude of crosstalk 151 increases between approximately 0.25 GHz and 1 GHz. Thus, based on this plot 300, the components of a victim communication signal $x(t)$ 214 that have frequencies between approximately 1 GHz and 4.25 GHz are particularly prone to crosstalk effects 151 from an aggressor communication signal $u(t)$ 215 with similar signal frequencies.

Furthermore, the fluctuations in the frequency response plot 300 at frequencies above 2 GHz illustrate that the crosstalk effect 151 is heavily influenced by other effects than simple capacitive coupling between a pair of pins. In other words, above 2 GHz, the plot 300 deviates from a classical capacitive coupling response, which typically asymptotically (and monotonically) increases with increased frequency. In contrast, the illustrated plot 300 exhibits a pattern of peaks and valleys at higher frequencies, such as a local minimum at approximately 4.6 GHz.

As described above, adequate crosstalk cancellation depends heavily on accurately modeling a system's crosstalk response. Crosstalk cancellation performance is particularly dependent upon model accuracy for frequencies in which the crosstalk effect is strong, namely for frequencies above approximately 1 GHz.

The higher order effects of the aforementioned peaks and valleys in the plot 300 are highly dependent on specific relative geometric relations between the victim signal path 130 and aggressor signal path 120 which are not known *a priori*, in general. In other words, deriving an accurate and sufficient crosstalk model based on geometric or
5 physical analysis of communication paths can be problematic without empirical data or test measurements regarding actual crosstalk impact on a signal.

Stated another way, the plot 300 of Figure 3 illustrates that the higher frequency components of a communication signal 214, 215 are particularly prone to crosstalk 151 and that modeling the crosstalk response 210 for these higher frequency components
10 involves addressing the inherently erratic nature of this high frequency response. Since an accurate model of a system's crosstalk response 210 can provide the basis for adequate crosstalk cancellation, such a model needs to accurately represent these higher-order, erratic response characteristics. While passive circuit analysis does not readily derive a model with requisite accuracy, actual signal responses can serve as a basis for
15 constructing an appropriate model.

In one exemplary embodiment of the present invention, a crosstalk model in a crosstalk cancellation device can be defined based on crosstalk measurement data such as the measurement data presented in the plot 300 illustrated in Figure 3. As an alternative to acquiring such measurement data in the laboratory, the data can be acquired during
20 field operations, for example by switching a crosstalk cancellation device into a calibration mode as will be discussed below in reference to Figure 9 and Figure 11.

Turning now to Figure 4, this figure illustrates a functional block diagram of a crosstalk cancellation system 400 according to an exemplary embodiment of the present invention. As described above, the present invention can provide crosstalk cancellation
25 in high-speed digital communication systems, such as the communication system 100 illustrated in Figures 1 and 2 and discussed above. More specifically, Figure 4 illustrates a crosstalk cancellation device or crosstalk canceller ("XTC") 401 arranged to cancel crosstalk 151 occurring in a backplane-to-line card connector 101b as discussed above with reference to Figures 1, 2, 3, and 4.

Digital data $x(t)$ 214 propagates in the victim channel 130 for reception by the victim receiver 105b. The victim channel 130 also carries the unwanted crosstalk signal $n(t)$ 230 that is derived from digital data $u(t)$ 215 output by the aggressor transmitter 104b and that is not intended for reception at the victim receiver 105b. The intended data stream signal $x(t)$ 214 and the crosstalk signal $n(t)$ 230 additively form the composite signal $y(t)$ 260. The crosstalk canceller 401 receives the composite signal $y(t)$ 260, corrects the crosstalk interference $n(t)$ 230 from this signal 260 via cancellation and outputs a corrected signal, $z(t)$ 420 for receipt by the victim receiver 105b. That is, the crosstalk canceller 401 applies an estimate of the actual crosstalk 230 to the signals 260 propagating in the victim channel 130 to effectively cancel the crosstalk signal elements 230 while leaving the desired data signal 214 essentially intact.

The steps that the crosstalk canceller 401 performs include:

- (i) accepting as separate inputs $y(t)$ 260 (the victim signal corrupted by crosstalk 151) and a representative portion of $u(t)$ 215 (the aggressor signal propagating on the aggressor channel 120 giving rise to the crosstalk signal 230);
- (ii) transforming the transmitted aggressor signal $u(t)$ 215 into crosstalk estimates that emulate the signal transformation 210 that actual occurs in the system 200 via the crosstalk effect 151;
- (iii) subtracting the modeled crosstalk from the victim $y(t)$ 260 to cancel its crosstalk signal $n(t)$ 230 component; and
- (iv) outputting the compensated signal $z(t)$ 420 to the victim receiver 105b, which can be a conventional receiver without specific technology for crosstalk compensation.

Turning now to Figure 5, this figure illustrates a functional block diagram of a crosstalk cancellation system 500 according to an exemplary embodiment of the present invention. More specifically, Figure 5 illustrates an architectural overview of an exemplary crosstalk canceller 401 that has three functional elements 501, 502, 503, a crosstalk model 501, a summation node 502, and a 503 controller, electronic control “mechanism”, or control module. The model 501 generates a crosstalk estimate

signal $w(t)$ 520, while the summation node 502 applies this crosstalk estimate 520 to the victim channel 130. The controller 503 adjusts parameters in the model 501 based on the output $z(t)$ 420 of the summation node 502.

The model 501 emulates the aggressor transfer function $H(f)$ 210 in the form of an adjustable frequency response function $G(f)$ 501. That is, the model 501 generates an artificial crosstalk signal $w(t)$ 520 that can be a model, simulation, estimate, or emulation of the actual, interfering crosstalk signal $n(t)$ 230 caused by electromagnetic coupling in the connector 102b between the aggressor channel 120 and the victim channel 130. The model frequency response $G(f)$ 501 effectively filters the aggressor data signal $u(t)$ 215 in a manner that applies a frequency dependent response similar to the plot 300 illustrated in Figure 3 and discussed above.

Because the same aggressor data stream $u(t)$ 215 drives both the actual crosstalk response $H(f)$ 210 and the crosstalk canceller' model 501, the output $w(t)$ 520 of the model 501 is, in the ideal case, equal to the aggressor signal component $n(t)$ 230. That is, $G(f)$ 501 equals $H(f)$ 210 in a theoretical or ideal case in which the environment is noise-free and all system parameters are known and modeled perfectly. Furthermore, in this ideal scenario, the respective output signals $n(t)$ 230 and $w(t)$ 520 of $H(f)$ 210 and $G(f)$ 501 would also be equal to one another. In a real-world situation having numerous unknown influences and indeterminate factors, $G(f)$ 501 approximates $H(f)$ 210 with sufficient precision and accuracy to support essentially error-free communications of high-speed data rates.

The difference node 502 subtracts the emulated aggressor signal $w(t)$ 520, or emulation signal 520, from the composite signal $y(t)$ 260, thus removing or reducing crosstalk interference from the received victim signal $y(t)$ 260. In a physical implementation functioning in a real-world operating environment, the model $G(f)$ 501 does not exactly match the true response $H(f)$ 210. The controller 503 adjusts the model 501 to minimize this error related to inaccuracies between the actual crosstalk effect $H(f)$ 210 and the emulated or modeled crosstalk effect $G(f)$ 501.

Implementation of the summation node 502 is usually straightforward to those skilled in the art. However, special attention should be paid to maintain high sensitivity

to the two inputs. It is not uncommon for the incurred, and thus the modeled, crosstalk signals 230, 520 to be small in amplitude, especially at high frequencies. While seemingly negligible at first glance, these high frequencies are often amplified via equalization devices (not illustrated). Thus, while the neglected high-frequency crosstalk
5 may be small before equalization, it can be very significant after equalization. The summation node should be implemented to accommodate such high-frequency response.

A portion of the compensated signal $z(t)$ 420 (i.e. the output of the difference node 502) is tapped off and fed to the controller 503, providing the controller 503 with essentially the same signal 420 that the victim receiver 105b receives. The controller
10 adjusts the parameters of the modeling filter 501, characterized by the response $G(f)$ 501, to maximize the goodness-of-fit to the actual response $H(f)$ 210. In particular, the controller 503 takes as input the crosstalk compensated signal $z(t)$ 420 and processes, monitors, or analyzes that signal 420 to determine signal fidelity. In other words, the controller 503 evaluates the model's performance by analyzing the extent to which the
15 model's output 520 has cancelled the crosstalk signal 230. The controller 503 also adjusts the model 501 to enhance crosstalk cancellation and to provide dynamic response to changing conditions.

Because the output of the controller 503 includes parameters of the modeling filter 501, the controller can adjust the modeled response $G(f)$ 420. Consequently, the
20 controller 503 can manipulate the modeling filter 501 to maximize the fidelity of the compensated signal 420, i.e. the match between $G(f)$ 420 and $H(f)$ 210, by minimizing crosstalk on $z(t)$ 420. Stated another way, the controller 503 monitors the corrected, crosstalk-cancelled signal $z(t)$ 420 and dynamically adjusts the crosstalk model $G(f)$ 420 to improve the crosstalk cancellation and enhance signal quality. Thus, in one exemplary
25 embodiment of the present invention, a crosstalk cancellation device 401 can include a feedback loop that adapts, self-corrects, or self configures crosstalk cancellation to compensate for modeling errors, fluctuating dynamic conditions, and other effects.

The system illustrated in Figure 5 can be implemented primarily using analog integrated circuitry to provide a relatively low degree of complexity, power consumption,
30 and cost. In one embodiment, the model 501 and difference node 502 are entirely analog.

In another embodiment, certain aspects of the model 501 are implemented digitally to exploit the digital nature of the aggressor data source 104b.

The controller 503 typically includes both analog and digital circuitry. Due to particular aspects of the analog pre-processing in the controller 503, this digital circuitry can operate at a low speed relative to the communication data rate and thus can facilitate practical implementation. In particular, the digital circuitry can operate at speeds that are orders of magnitude less than the channel baud rate. In one exemplary embodiment of the present invention, a digital circuit in the controller 503 operates at least one order of magnitude below the channel baud rate. In one exemplary embodiment of the present invention, a digital circuit in the controller 503 operates at least two orders of magnitude below the channel baud rate. In one exemplary embodiment of the present invention, a digital circuit in the controller 503 operates at least three orders of magnitude below the channel baud rate. Further details exemplary embodiments of the controller 503 and the model 501 that together yield a low-power and low-cost crosstalk cancellation solution are discussed in more detail below.

Turning now to Figure 6, this figure is a functional block diagram of a tapped delay line filter 600 according to an exemplary embodiment of the present invention. A tapped delay line filter 600 is a device that generates an output signal 620 from an input signal 215 by delaying the input signal 215 through a series of delay stages 601a, 601b, 601c; scaling the output of each delay stage 601a, 601b, 601c, typically with an amplifier 602a, 602b, 602c, 602d; and adding or otherwise combining these scaled outputs. The tapped delay line filter 600 can be an analog component of the model 501 that generates a signal $v(t)$ 620 having a shape or waveform approximating that of the imposed crosstalk signal $n(t)$ 230. That is, the tapped delay line filter 600 can be an exemplary waveform shaper that is implemented via analog components.

As described above, accurately modeling the actual crosstalk response 210 facilitates adequate removal of crosstalk interference 230 via crosstalk cancellation. If a crosstalk cancellation device (not illustrated) were based on an inaccurate crosstalk model (not illustrated) such a device might degrade, rather than improve, signal quality. For example, as a result of an erroneous model, a "correction" signal intended to cancel

crosstalk might add interference to a received victim signal while leaving the crosstalk signal that is targeted for cancellation essentially intact. Thus, a crosstalk model, for example based on a filtering mechanism, should have sufficient flexibility to support modeling a variety of crosstalk transfer functions that may be encountered in an application. That is, a flexible crosstalk model is desirable over a rigid model that cannot readily adapt to various applications, operating conditions, and environments, for example.

In one exemplary embodiment of the present invention, as illustrated in Figure 6, an analog tapped delay line filter 600 (also known as transversal filter) with electrically controllable gain coefficients 602a, 602b, 602c, 602d; models the aggressor crosstalk transfer function 210. This filter 600 can provide a desirable level of flexibility and adaptability that supports a wide range of operating conditions and situations. More specifically, the tapped delay line filter 600 can generate a waveform approximating the waveform of the crosstalk signal 230 imposed on the victim channel 130.

The illustrated filter 600 is an exemplary tapped delay line filter with N delay elements 601a, 601b, 601c (each providing time delay δ (delta)) and corresponding variable coefficient amplifiers 602a, 602b, 602c, 602d with coefficients α_n (alpha_n) for $n=0, \dots, N$. The output $v(t)$ 620 of the tapped delay filter 600 can be written as

$$v(t) = \alpha_0 u(t) + \alpha_1 u(t-\delta) + \dots + \alpha_N u(t-N\delta).$$

Changing the values of the gain coefficients $\alpha_0, \alpha_1, \alpha_2 \dots \alpha_n$ (alpha₀, alpha₁, alpha₂ ... alpha_n) can cause a corresponding change in the response of the filter 600. The tapped delay line filter 600 can model the aggressor's impulse response for up to $N\delta$ (N times delta), that is, up to the temporal span of the filter 600. Additionally, the frequency content of the aggressor response 210 (as illustrated in Figure 3 and discussed above) can be modeled up to a frequency of $f=1/(2\delta)$ (frequency equals the reciprocal of two times delta). Thus, δ (delta) should be chosen such that the highest signal frequency of interest in the victim signal $x(t)$ 214 is less than $f=1/(2\delta)$ (frequency equals the reciprocal of two times delta). Furthermore, N should be chosen so that the majority of the aggressor impulse response is contained within a temporal span of $N\delta$ (N times delta).

Equivalently, the aggressor frequency response 210 should not exhibit large fluctuations below frequencies of $f=1/(N\delta)$ (frequency equals the reciprocal of N times delta). These conditions for selecting N and δ (delta) contrast with the aggressor signal's conditions. It is not critical if aggressor noise remains above the specified frequency, because a well
5 designed receiver can readily suppress these higher frequencies without degrading victim signal quality.

While a tapped delay line filter 600 can emulate, estimate, or mimic pulse shaping caused by the aggressor response 210, this filter 600 typically cannot adequately address highly variable temporal delay without an unwieldy number of taps or delay stages.
10 Temporal delay is directly associated with the length of the signal path that spans between (i) the circuit tap that directs a portion of the aggressor data signal $u(t)$ 215 to the crosstalk canceller 401 and (ii) the summation node 502 in the crosstalk canceller 401, as illustrated in Figure 5 and discussed above. More specifically, the modeled temporal delay should closely approximate the temporal delay of the actual crosstalk signal
15 $n(t)$ 230 so that the modeled and actual signals 230, 520 are properly synchronized or timed with respect to one another for effective mutual cancellation. While the output 620 of the tapped delay line filter 600 can be directly used as the output $w(t)$ 520 of the model 501, synchronizing the tapped delay line filter's output 620 with the crosstalk signal 230 on the victim channel 130 can enhance crosstalk cancellation, provide
20 heightened signal fidelity to the victim receiver 105b, and improve overall modeling flexibility.

Because the locations of the coupling points of both the actual crosstalk signal 230 and its modeled counterpart 520 can be significantly variable among victim-aggressor pairs, their respective delays can be ill-defined or subject to uncertainty. Even
25 in the relatively simple case of dominant coupling via the backplane-linecard connector 102b, the signal path length on the linecard 101b is often variable. Thus, the temporal delay can be difficult to predict without specific knowledge and analysis of linecard layout. To address this uncertainty in temporal delay, an adjustable delay 701 can be incorporated into the cross talk modeling filter 501 as illustrated in Figure 7.

Turning now to Figure 7, this figure is a functional block diagram of a crosstalk modeling filter ("XTMF") 501 of a crosstalk cancellation device 401 with an adjustable delay 701 according to an exemplary embodiment of the present invention. The adjustable delay 701 can either precede or succeed (as shown in Figure 7) the tapped delay line filter 600. In one exemplary embodiment of the present invention, placing the adjustable delay 701 on the input side of the analog tapped delay line filter 600, rather than the output side as illustrated, can simplify the implementation. This simplification can result from the discrete nature of the digital signal $u(t)$ 215 wherein signal linearity can be readily maintained by quantizing or hard-limiting the output of the delay device 701. Alternatively, if the adjustable delay 701 follows the tapped delay line filter 600, in accord with the illustrated configuration, the signal $v(t)$ 620 is analog at the input to the adjustable delay 701. Inputting an analog signal into the adjustable delay 701 can impose a need for a linear response over a broad range of signal values and frequencies, which can be difficult to achieve for large delay values.

While the tapped delay line filter 600 outputs a correction signal $w(t)$ 520 that approximates the crosstalk signal $n(t)$ 230 undesirably propagating on the victim channel 130 alongside the intended data signal $x(t)$ 214, the adjustable delay 701 synchronizes the waveform of the correction signal 520 with the waveform of the undesirable crosstalk signal 230. That is, the adjustable delay 701 times or coordinates the correction signal 520 so it temporally matches and is synchronized with the actual crosstalk interference 230.

Based on the functions of the tapped delay line filter 600 and the adjustable delay 701, the crosstalk modeling filter 501 outputs a cancellation signal $w(t)$ 520 having form and timing accurately matching the actual crosstalk signal $n(t)$ 230. When inserted into or applied to the victim channel 130 via the subtraction node 502, as illustrated in Figure 5 and discussed above, the cancellation signal $w(t)$ 520 negates the actual crosstalk signal 230 and thereby enhances the quality of the communication signal $z(t)$ 420 delivered to the victim receiver 105b.

As discussed in further detail above with reference to Figure 5 and below with reference to Figure 8, the controller 503 adjusts the tapped delay line filter 600 and the

adjustable delay 701 to fine tune their respective performances and to enhance the fidelity of the corrected signal 420 delivered to the victim receiver 105b.

Turning now to Figure 8, this figure is a functional block diagram of a crosstalk modeling filter 501' of a crosstalk cancellation device 800 with a high-pass filter 801 according to an exemplary embodiment of the present invention. The high-pass filter 801 is typically a fixed or non-adjustable filter. In the configuration of the exemplary embodiment of the illustrated in Figure 8, the adjustable delay 701 feeds the tapped delay line filter 600, thus offering advantages, as discussed above in reference to Figure 7, for certain applications.

Including the optional high-pass filter 801 in the exemplary crosstalk modeling filter 501', as illustrated in Figure 7, can enhance performance in some applications or operating environments. A high-pass filter 801 is a device that receives a signal having a range of frequency components, attenuates frequency components below a frequency threshold, and transmits frequency components above the frequency threshold.

While tapped delay line filters 600 have a flexible modeling response over the frequency range

$$1/(N\delta) < f < 1/(2\delta),$$

they are often less flexible at lower frequencies such as $f < 1/(N\delta)$ (frequencies less than the reciprocal of two times delta). Thus, accurately modeling low-frequencies characteristics of the crosstalk response 210 may require a large number N of filter taps that increase filter complexity or may require a longer delay increment δ (delta) that reduces high-frequency flexibility. In many applications, it is preferable to avoid such trade-offs. As discussed above with reference to Figure 3, for electrical systems, low-frequency crosstalk characteristics are usually dominated by capacitive coupling effects and can consequently be accurately modeled with a high-pass filter such as a simple first-order resistor-capacitor ("RC") high-pass filter. That is, inserting the high-pass filter 801 into the crosstalk modeling filter 801 can provide a high level of performance without requiring a cumbersome or expensive number of tap filters in the tapped delay line filter 600.

Similar to the exemplary embodiment of the crosstalk modeling filter 501 depicted in Figure 7, the ordering of the tapped delay line filter 600, the adjustable delay 701, and high-pass filter 801 can be permuted to support various arrangements. That is, the present invention supports arranging physical components corresponding to each of the functional blocks 701, 600, 801 illustrated in Figure 8 in any parallel or series configuration that provides acceptable performance for an intended application. Nevertheless, certain configurations or ordering may provide certain advantages or tradeoffs for select application situations as compared to other configurations.

The exemplary inline configuration illustrated in Figure 8 places the adjustable delay 701 on the input side of the tapped delay line filter 600 and the high-pass filter 801 on the output side of the tapped delay line filter 600. With this ordering, the implementation of the adjustable delay 701 can be simplified by exploiting the discrete-amplitude nature of both its input and output signal. The tapped delay line filter 600 can also exploit, via digital delay elements, the discrete-amplitude input provided from the adjustable delay 701. In its RC implementation, the high-pass filter 801 is an analog device that does not receive benefit from providing it with a discrete amplitude input. Thus, there is typically no drawback to placing the high-pass filter 801 at the output side of the crosstalk modeling filter 501 or in another position.

As discussed above with reference to Figure 5, the control module 503 takes as input the crosstalk compensated signal $z(t)$ 420 and outputs control signals 820, 830 to adjust the crosstalk response model 501. The control module's outputs 820, 830 to the crosstalk modeling filter 501 comprises: (i) a "delay control" signal 830 to control the time delay implemented by the adjustable delay component 701; and (ii) a set of "filter control" signals 820 to control the gains on the variable coefficient amplifiers 602a-d in the tapped delay line filter 600. That is, the controller 503 outputs modeling parameters to the tapped delay line filter 600 and timing parameters to the adjustable delay 701.

These output control values are determined based on observation, processing, and/or analysis of the compensated signal $z(t)$ 420. U.S. Nonprovisional Patent Application Serial No. 10/108,598, entitled "Method and System for Decoding Multilevel Signals" and filed on March 28, 2002, discloses a viable exemplary system and method

for assessing signal fidelity. Commonly owned U.S. Nonprovisional Patent Application Serial No. 10/620,477, entitled "Adaptive Noise Filtering and Equalization for Optimal High Speed Multilevel Signal Decoding" and filed on July 15, 2003, discloses a viable exemplary system and method for controlling device parameters of the crosstalk modeling filter 501. The disclosures of U.S. Patent Application Serial No. 10/108,598 and U.S. Patent Application Serial No. 10/620,477 are hereby fully incorporated by reference. One or more of the crosstalk model 501, the tapped delay line filter 600, and the adjustable delay 701 can each be controlled and/or adjusted using a method and/or system disclosed in U.S. Patent Application Serial Number 10/108,598 or U.S. Patent Application Serial Number 10/620,477. The temporal delay adjustment of the adjustable delay 701 can be determined by treating the delay control as a variable that is swept through its entire range of potential values following the disclosure of these patent applications, for example.

Turning now to Figure 9, this figure illustrates an exemplary system 900 for controlling a crosstalk model 501 such as the exemplary crosstalk modeling filter 501 illustrated in Figure 8 or the exemplary crosstalk modeling filter 501 illustrated in Figure 7 and their associated adjustable delays 701. More specifically, Figure 9 is a functional block diagram of a control module 900 of a crosstalk cancellation device 401 according to an exemplary embodiment of the present invention. The exemplary controller 900 illustrated in Figure 9 facilitates relatively simple theoretical analysis and implementation and, in that regard, can offer benefit to certain application over the control methods and systems disclosed in U.S. Patent Application Serial Numbers 10/620,477 and U.S. Patent Application Serial Numbers 10/108,598 that are discussed above.

The controller 900 of Figure 9 includes a filter 901, having a frequency transfer response $P(f)$, that receives the crosstalk-cancelled signal $z(t)$ 420 destined for reception by the victim receiver 105b. Filter 901 can be a spectral weighting filter based on this frequency transfer response. The output of this filter 901 couples to a power detecting or signal squaring device 902, which provides an output to a low-pass filter 903. A low-pass filter 903 is a device that receives a signal having a range of frequency components,

attenuates frequency components above a frequency threshold, and transmits frequency components below the frequency threshold.

An analog-to-digital converter ("ADC") receives the low-pass filter's output and generates a corresponding digital signal which feeds into the digital controller 905. The digital controller 905 in turn generates digital control signals for each of the adjustable delay 701 and the tapped delay line filter 600. Respective digital-to-analog converters ("DACs") 906a, 906b convert these signals into the analog domain for respective transmission over a delay control line 830 and a filter control line 820. The analog delay control signal adjusts the adjustable delay 701, while the analog filter control signal adjusts the tapped delay line filter 600.

It will be useful to discuss a simple operational example in which crosstalk is imposed on a channel that is in a temporary condition of not carrying data. More specifically, consider a case in which the victim transmitter 104a does not transmit any data while the aggressing transmitter 104b is sending data with a broad spectral content or range of signal frequencies, such as pseudo-random or coded pseudo-random data. That is, referring briefly back to Figure 5, the signal $x(t)$ 214 is essentially zero, while $u(t)$ 215 is a digital data signal having broad analog spectral content resulting from randomly varying digital data patterns. In this case, the signal $y(t)$ 260 is simply the incurred aggressor $n(t)$ 230, and the signal $w(t)$ 520 is the modeled aggressor. Thus, the signal $z(t)$ 420 is actually the modeling error of the cancellation device. In the theoretical and ideal situation of perfect crosstalk cancellation, $z(t)$ 420 is zero.

In other words, transmitting an essentially uniform voltage on the victim channel 130 while transmitting signals having a broad range of frequencies on the aggressor channel 120 provides essentially pure crosstalk on the victim channel 130 and $n(t)$ 230 equals $y(t)$ 260. If the crosstalk canceller 401 outputs a cancellation signal $w(t)$ 520 also equaling the pure crosstalk signal $n(t)$ 230, $z(t)$ 420 will have essentially no signal energy. Thus, in this state, signal energy in $z(t)$ 420 is indicative of modeling or delay inaccuracies in the crosstalk modeling filter 501.

The control module 900 can implement this state of transmitting a defined signal on the aggressor channel 120 and transmitting a constant voltage or essentially no data

signal on the victim channel 130. The control module 900 can then adjust the adjustable parameters of the crosstalk modeling filter 501 to minimize the signal $z(t)$ 420 received by the victim receiver 105b, thereby providing a crosstalk cancellation signal $w(t)$ 520 that matches the actual crosstalk signal $n(t)$ 230 and further providing a modeled crosstalk response $G(f)$ 501 that effectively matches the actual crosstalk response $H(f)$ 210. More generally, the control module 900 cause transmission of defined or known signal patterns on the aggressor channel 120, the victim channel 130, or both the aggressor channel 120 and the victim channel 130 to characterize the crosstalk effect 151 and to control, optimize, or adjust crosstalk cancellation or another form of crosstalk compensation. Further, the control module 900 can have a learning or adaptive mode in the form of a setup mode or a self-configuration procedure and can implement automatic or self calibration.

Referring to Figure 9 and generalizing beyond the example of imposing crosstalk on a channel void of data, this error signal $z(t)$ 420 can be spectrally weighted with an optional filter 901, whose response is denoted as $P(f)$, to emphasize any higher importance of certain frequencies over others. For example, it may be desired to high-pass filter the error signal $z(t)$ 420 to emulate the effect of equalization in the victim receiver 105b. The (potentially spectral weighted) error signal $z(t)$ 420 is then squared or power-detected, i.e. the output of the squaring device 902 is the signal power. The power signal is then passed through a low-pass filter 903 (or integrator) with a relatively low cutoff-frequency to obtain the integrated power, i.e. energy, of the error signal $z(t)$ 420. Thus, the signal at this point corresponds to an analog estimate of the statistical variance (i.e. the square of the standard deviation) of the error signal $z(t)$ 420.

As familiar to those skilled in the art, the error variance is a useful metric for gauging fidelity. Because the cutoff frequency of the low-pass filter 903 is at a very low frequency (typical orders of magnitude below the symbol transmission rate), the variance signal is nearly a constant after the transient effects of any modeling filter changes decay away. Thus, the analog variance signal can be sampled with a simple low-speed high-resolution analog-to-digital converter 904. The digitized signal output by the analog-to-digital converter 904 provides the error variance information to a simple microprocessor,

state machine, finite state machine, digital controller, or similar device (referred to herein as a "digital controller") 905. After recording the error-variance for the current set of response modeling parameters, the digital controller 905 may then specify a new filter configuration by digitally outputting the new parameters to a set of DACs 906 which
5 provide the corresponding analog signal to the aggressor emulation module 501.

As the digital controller 905 is able to both (i) set the parameters of the crosstalk modeling filter 501 and (ii) directly observe the effect of the current parameters on the modeling error variance, the digital controller 905 can find a parameter set that maximizes the fit of the aggressor response model 501 to the actual response 210.
10 Because trial-and-error processing is not overly complicated, all combinations of model parameters can be tested in many instances. However, other empirical search/optimization methodologies known to those skilled in the art may be alternatively employed. In one exemplary embodiment of the present invention, a coordinate-descent approach, as described in U.S. Patent Application No. 10/620,477, discussed above,
15 provides search and optimization to identify acceptable model parameters.

As discussed above, the control module 900 can comprise a combination of analog and digital circuitry to provide a practical control implementation. Filter 901 and power detecting device 902 collectively input and output a high-speed analog signal. The low-pass filter 903 takes as input a high-speed analog signal and outputs a low-speed
20 analog signal. Filter 901, power detecting device 902, and low-pass filter 903 collectively take a projection of a high-speed signal onto a low-speed signal by extracting the relevant statistical information from the high-speed signal and presenting it in a more concise form. The ADC 904 takes this low-speed analog signal as an input and outputs a corresponding digitized approximation. Consequently, the controller 905 receives and
25 processes this low-speed digital signal. Because the digital signal is low-speed, the associated processing circuitry is less complex than would be required if the signal was high-speed. The digital controller 905 outputs low-speed digital control signals to the digital-to-analog converters 906a, 906b which in turn output low-speed analog signals. As a result of tandem simple high-speed analog preprocessing and low-speed digital
30 processing, the control module 900 provides signal analysis based on a powerful

statistical characterization and implements a robust control methodology with relatively little circuit complexity, which are factors that can facilitate practical crosstalk cancellation in high-speed communications systems.

While the illustration in Figure 9 employs a power-detecting (or signal squaring) device 902 to generate the error-variance, a full-wave-rectifier (which takes the absolute value of the signal) may be used as an alternative. For an implementation based on a full-wave-rectifier, the output of the low-pass filter 903 will now no longer correspond to error variance, but will nevertheless represent a valid fidelity criterion. In particular, it is the 1-norm of the error signal 420, and thus the fidelity metric still has suitable mathematical properties. Those skilled in the art appreciate that determining the "1-norm" of a signal typically comprises integrating the absolute value of the control signal. This substitution may be advantageous for certain applications because: (i) the 1-norm signal may have a reduced dynamic range (thus relaxing resolution constraints on the analog-to-digital converter 904); and (ii) full-wave-rectifiers may be easier to implement than power-detectors. Such modifications are considered within the scope of the present invention.

Similarly, the power detector 902 may also be replaced with a half-wave-rectifier or any like device that is used to assess signal magnitude. It will also be appreciated by those skilled in the art that the division of the crosstalk canceller 401 into functional blocks, modules, and respective sub-modules as illustrated in Figures 5 through 9 are conceptual and do not necessarily indicate hard boundaries of functionality or physical groupings of components. Rather, representation of the exemplary embodiments as illustrations based on functional block diagrams facilitates describing an exemplary embodiment of the present invention. In practice, these modules may be combined, divided, and otherwise repartitioned into other modules without deviating from the scope of the present invention.

In one exemplary embodiment of the present invention, a crosstalk cancellation system is a single integrated circuit ("IC"), such as a monolithic IC. Each of a crosstalk cancellation device, a control module, and a crosstalk modeling filter can also be single

ICs. Such ICs can be complementary metal oxide semiconductor ("CMOS") ICs and can be fabricated in a 0.18 micron process, for example.

A process for canceling crosstalk and a process for calibrating a crosstalk canceller will now be described with respective reference to Figure 10 and Figure 11.

5 Certain steps in the processes described herein must naturally precede others for the present invention to function as described. However, the present invention is not limited to the order of the steps described if such order or sequence does not alter the functionality of the present invention. That is, it is recognized that some steps may be performed before or after other steps or in parallel with other steps without departing
10 from the scope and spirit of the present invention.

Turning now to Figure 10, this figure is a flow chart illustrating Process 1000, entitled Cancel Crosstalk, for canceling crosstalk 151 according to an exemplary embodiment of the present invention. At Step 1010, the first step in Process 1000, the aggressor transmitter 104b transmits an aggressor communication signal $u(t)$ 215 on the
15 aggressor channel 120. This communication signal 215 can be an analog or a digital signal carrying data.

At Step 1015, the crosstalk effect 151 couples energy from the aggressor communication signal $u(t)$ 215 into the victim channel 130 as crosstalk $n(t)$ 230. The coupling mechanism can be electromagnetic coupling, as in the exemplary case of
20 electrical data signals propagating on a backplane 103, or another optical or electrical crosstalk mechanism. The energy transfer of the crosstalk effect 151 generates the crosstalk signal $n(t)$ 215 in the victim channel 130 in a manner that results in signal propagation towards the victim receiver 105b.

At Step 1020, the victim transmitter 104a transmits the victim communication
25 signal $x(t)$ 214 on the victim channel 130. The victim communication signal 214 can be either an analog or a digital signal. At Step 1025, the crosstalk signal $n(t)$ 230 coexists or mixes with the victim communication signal $x(t)$ 214 in the victim channel 130. The composite signal $y(t)$ 260 results from the combination of these signals 214, 230.

At Step 1030, the crosstalk model 501 acquires a sample of the aggressor
30 communication signal $u(t)$ 215. In other words, a tap or other node directs a

representative portion of the aggressor communication signal 215 to the crosstalk canceller 401 for reception and processing by the crosstalk model 501.

At Step 1035, the crosstalk model 501 processes the sampled portion of the aggressor communication signal $u(t)$ 215 via the tapped delay line filter 600. Modeling parameters, such as the gain or scaling constants of the tapped delay line filter 600, provide the basis for generating a waveform estimate $v(t)$ 620 of the crosstalk signal $n(t)$ 215. More specifically, the coefficients $\alpha_0, \alpha_1, \alpha_2 \dots \alpha_n$ ($\alpha_0, \alpha_1, \alpha_2 \dots \alpha_n$) of the variable coefficient amplifiers 602a, 602b, 602c, 602d in the tapped delay line filter define a waveform $v(t)$ 620 approximating the crosstalk signal 215.

At Step 1040, the adjustable delay 701 in the crosstalk model 501 applies a time delay to the waveform estimate $v(t)$ 620 to synchronize this waveform 620 with the interfering crosstalk signal $n(t)$ 230 propagating in the victim channel 130. At Step 1045, the summation node 502 of the crosstalk canceller 401 applies the resulting crosstalk cancellation signal $w(t)$ 520 to the victim channel 130 and the combined crosstalk and communication signal $y(t)$ 260 propagating therein. The crosstalk cancellation signal $w(t)$ 520 cancels at least a portion of the crosstalk signal component $w(t)$ 520 propagating in the victim channel 130. Reducing this crosstalk interference 520 improves signal fidelity in the communication signal $z(t)$ 420 that is output by the crosstalk canceller 410 for delivery to the victim receiver 105b.

At Step 1050, the controller 503 processes or analyzes the crosstalk compensated signal $z(t)$ 420 to determine effectiveness of crosstalk cancellation. In other words, the controller 503 assesses signal fidelity to determine if the crosstalk canceller is applying a crosstalk cancellation signal $w(t)$ 520 that accurately matches the actual crosstalk $n(t)$ 230, both in waveform and in timing.

At Step 1055, the controller 503 adjusts the modeling parameters, specifically the coefficients of the variable coefficient amplifiers 602a, 602b, 602c, 602d in the tapped delay line filter 600, to optimize the waveform match between the crosstalk cancellation signal $w(t)$ 520 and the actual crosstalk signal $n(t)$ 230. The controller 503 further adjust the variable or adjustable time delay of the adjustable delay 701 to synchronize the crosstalk cancellation signal $w(t)$ 520 with the actual crosstalk signal $n(t)$ 230. That is,

the controller 503 adjusts the operation of the crosstalk canceller 401 by implementing parameter adjustments to the crosstalk modeling filter 501 to enhance the fidelity of the net communication signal $z(t)$ 420 delivered to the victim receiver 105b.

Following Step 1055, Process 1000 iterates Steps 1010-1055. The crosstalk
5 canceller 401 continues canceling crosstalk 230 and implementing adaptive responses to dynamic conditions, thereby providing an ongoing high level of communication signal fidelity.

Turning now to Figure 11, this figure is a flow chart illustrating Process 1100,
entitled Calibrate Crosstalk Canceller, for calibrating a crosstalk cancellation device 401
10 according to an exemplary embodiment of the present invention. At Step 1110, the first step in Process 1100, the controller 503 initiates a calibration sequence. The controller 900 instructs the aggressor transmitter 104b to output a signal having a known or defined test pattern, for example a random or pseudo random bit pattern of data, onto the aggressor channel 120. This test or calibration signal can have the format of an
15 aggressor communication signal $u(t)$ 215 or can be uniquely formatted for characterizing the crosstalk response $H(f)$ 210. That is, the controller 900 can control transmission of signals having predetermined voltage patterns on the aggressor channel 120.

At Step 1115, the controller 900 instructs the victim transmitter 104b to output a known victim test or reference signal onto the victim channel 130. The test signal can be
20 a predetermined communication signal or simply a constant voltage, null of data. Sending a known test signal on the victim channel 130 facilitates isolating the crosstalk response $H(f)$ 210 from other effects that may generate signal distortion on the victim channel 130. That is, the controller 900 can control transmission of signals having predetermined voltage patterns on the victim channel 130.

25 At Step 1120, crosstalk $n(t)$ 230 from the known aggressor signal $u(t)$ 215 couples into the victim channel 130. With the victim channel 130 carrying a constant voltage as the victim signal $x(t)$ 214, the composite communication and crosstalk signal $y(t)$ 260 on the victim channel 130 is essentially the crosstalk signal $n(t)$ 230.

At Step 1125, the crosstalk canceller 401 generates an estimate $w(t)$ 520 of the
30 crosstalk signal $n(t)$ 230 for crosstalk cancellation. The crosstalk canceller 401 generates

this estimate 520 using modeling and delay parameters that result in a waveform and timing match between the crosstalk signal $n(t)$ 230 and the crosstalk cancellation signal $w(t)$ 520. The crosstalk compensator 401 applies the crosstalk estimate 520 to the victim channel 130 and cancels at least a portion of the crosstalk 230 propagating thereon. The
5 resulting crosstalk-cancelled signal $z(t)$ 420 propagates to the victim receiver 105b.

At Step 1130, the controller 503 processes and analyzes the crosstalk-cancelled signal $z(t)$ 420 output by the crosstalk canceller 401. Based on the analysis, the controller 503 adjusts the modeling and delay parameters to minimize the energy in the crosstalk cancelled signal $z(t)$ 420. That is, the controller 503 varies the operational
10 parameters of the crosstalk canceller 401 towards reducing the residual crosstalk. This control action matches the crosstalk compensation signal $w(t)$ 520 with the actual crosstalk $n(t)$ 230 imposed on the victim channel 130.

At Step 1140, the controller 503 completes the calibration cycle and provides notification to the aggressor and victim transmitters 104a, 104b that the crosstalk
15 canceller 401 is set to process live data. In response to this notification, at Step 1145 the victim transmitter 104a and the aggressor transmitter 104b each transmit live data on their respective channels 130, 120.

At Step 1150, crosstalk 230 from live data 215 transmitting on the aggressor channel 120 couples into the victim channel 130. At Step 1155, the crosstalk
20 canceller 401 processes a sample of the live data 215 transmitting in the aggressor channel 120 and generates an emulation or estimate 520 of the crosstalk 230 using the modeling and delay parameters defined or updated during calibration.

At Step 1160, the crosstalk canceller 401 applies the crosstalk estimate 520 to the victim channel 130 for crosstalk cancellation and presents the victim receiver 105 with a
25 high-fidelity signal. Process 1100 ends following Step 1160. The controller 503 can repeat the calibration procedure at defined or regular time intervals or when the controller's monitoring capability determines that signal fidelity is impaired or as fallen below a threshold.

Turning now to Figures 12A and 12B, these figures respectively illustrate testing
30 data of a communication system before and after implementing crosstalk cancellation

according to an exemplary embodiment of the present invention. These figures present eye diagrams 1200, 1250 of measured data captured under laboratory conditions. As is well known to those skilled in the art, eye diagrams 1200, 1250 provide a visual indication of signal quality. The level of openness of an "eye" 1225, 1275 in an eye diagram 1200, 1250 correlates with the level of signal quality. That is, a noisy, distorted, or closed eye in an eye diagram typically indicates signal impairment.

Figure 12A is an eye diagram 1200 from a 5 Gigabits per second binary communication system operating under laboratory conditions believed to be representative of field conditions. The victim signal 130 has an amplitude of 800 millivolts, while the aggressor signal 120 has an amplitude of 1,200 millivolts. Figure 12A illustrates the eye diagram 1200 of the received signal 260 after equalization and limiting amplification but without crosstalk compensation. Figure 12B illustrates the eye diagram 1250 of the received signal 420 after application of crosstalk cancellation in accordance with an exemplary embodiment of the present invention followed by equalization and limiting amplification. As with the eye diagram of Figure 12A, the victim signal 130 has an amplitude of 800 millivolts, while the aggressor signal 120 has an amplitude of 1,200 millivolts.

Because the signal path includes a limiting amplifier in both the crosstalk-corrected eye diagram 1250 and the eye diagram 1200 without crosstalk compensation, the thicknesses of the horizontal "eye-lids" at the top and bottom of each eye diagram 1200, 1250 do not provide a useful gauge of signal quality. Rather the signal performance enhancement provided by crosstalk cancellation is evident from the wide open eye 1275 in the crosstalk-corrected eye diagram 1250 relative to the narrow, noisy eye 1225 of the eye diagram 1225 without crosstalk correction.

To further characterize communication performance improvement achieved by crosstalk cancellation in accordance with an exemplary embodiment of the present invention, bit error rate measurements were acquired from this test system under the same test conditions, before and after crosstalk cancellation. Without crosstalk cancellation, the communication system exhibited an average of one bit error for every 100,000 bits

transmitted. With crosstalk cancellation, the communication system exhibited an average of one bit error for every 100,000,000,000,000 bits transmitted.

Although a system in accordance with the present invention can comprise a circuit that cancels, corrects, or compensates for crosstalk imposed on one communication signal
5 by another signal, those skilled in the art will appreciate that the present invention is not limited to this application and that the embodiments described herein are illustrative and not restrictive. Furthermore, it should be understood that various other alternatives to the embodiments of the invention described here may be employed in practicing the invention. The scope of the invention is intended to be limited only by the claims below.

CLAIMS

What is claimed is:

1. A signal processing system, for applying a crosstalk estimate to a first communication channel to compensate for crosstalk coupled into the first communication channel from a second communication channel, comprising:
 - a model, coupled to the second communication channel, operative to generate the crosstalk estimate, comprising:
 - a waveform shaper comprising a plurality of delay stages coupled to a plurality of respective amplifiers; and
 - an adjustable delay coupled to the waveform shaper; and
 - a controller, operative to process crosstalk compensated communication signals and adjust the waveform shaper and the adjustable delay.
2. The signal processing system of Claim 1, wherein the waveform shaper comprises a tapped delay line filter.
3. The signal processing system of Claim 1, wherein the adjustable delay receives input from the waveform shaper.
4. The signal processing system of Claim 1, wherein the adjustable delay provides input to the waveform shaper.
5. The signal processing system of Claim 1, wherein the model further comprises a fixed filter, coupled to at least one of the waveform shaper and the adjustable delay, operative to transmit frequencies above a threshold frequency and attenuate frequencies below the threshold frequency.
6. The signal processing system of Claim 1, wherein the controller comprises a digital controller.

7. The signal processing system of Claim 1, wherein the controller is further operative to control transmission of signals having predetermined voltage patterns on the first and second communication channels.

5 8. The signal processing system of Claim 1, wherein the controller is operative to calibrate the model.

9. A method for processing a first communication signal having crosstalk from a second communication signal comprising the steps of:

sampling the second communication signal;

5 processing the sample of the second communication signal to generate a crosstalk emulation signal;

applying the crosstalk emulation signal to the first communication signal to cancel at least some of the crosstalk;

generating a third communication signal based on the application of the crosstalk emulation signal;

10 analyzing the third communication signal; and

adjusting the crosstalk emulation signal based on the analysis.

10. The method of Claim 9, wherein:

15 the processing step comprises processing the sample with a model comprising at least one modeling parameter; and

the adjusting step comprises adjusting the at least one modeling parameter.

11. The method of Claim 9, wherein the adjusting step comprises timing the crosstalk emulation signal to match the crosstalk.

20

12. The method of Claim 9, wherein:

the processing step comprises synchronizing the crosstalk emulation signal with the crosstalk to a level of synchronization; and

the adjusting step comprises improving the level of synchronization.

25

13. The method of Claim 9, wherein:

generating the crosstalk emulation signal comprises generating a waveform differing from the crosstalk by a difference; and

the adjusting step comprises reducing the difference.

14. The method of Claim 9, wherein: analyzing the third communication signal comprises monitoring for residual crosstalk in the third communication signal; and adjusting the crosstalk emulation signal comprises reducing the residual crosstalk.

- 5 15. The method of Claim 9, wherein applying the crosstalk emulation signal to the first communication signal comprises subtracting the crosstalk emulation signal from the first communication signal.

16. A signal processing method, comprising the steps of:
transmitting a test signal over a first communication channel;
coupling a portion of the test signal from the first communication channel into a
second communication channel via a crosstalk effect;
5 defining a model of the crosstalk effect based on processing the portion of the test
signal coupled into the second communication channel via the crosstalk effect;
transmitting a first communication signal over the first communication channel;
coupling a portion of the first communication signal into the second
communication channel via the crosstalk effect;
10 transmitting a second communication signal over the second communication
channel;
processing a portion of the first communication signal with the model and
outputting an estimate of the portion of the first communication signal coupled into the
second communication signal via the crosstalk effect; and
15 compensating for the crosstalk effect by applying the estimate to the second
communication channel.

17. The method of Claim 16, wherein the step of transmitting the test signal
over the first communication channel further comprises transmitting data on the first
20 communication channel and providing an essentially uniform voltage on the second
communication channel.

18. The method of Claim 16, wherein defining the model of the crosstalk
effect comprises adjusting a modeling parameter of the model.

25

19. The method of Claim 16, wherein defining the model of the crosstalk
effect comprises adjusting a signal delay of the model.

20. The method of Claim 16, wherein defining the model of the crosstalk
30 effect comprises setting a gain of the model.

21. The method of Claim 16, wherein applying the estimate to the second communication channel comprises subtracting the estimate from the second communication signal and the portion of the first communication signal coupled into the
5 second communication channel via the crosstalk effect.

22. A circuit, for correcting crosstalk coupled from a first communication channel into a second communication channel, comprising:

a modeling filter, coupled to the first communication channel, operative to generate an estimate of the crosstalk, comprising:

- 5 an adjustable analog filter, controllable by a modeling input; and
- an adjustable delay coupled to the adjustable analog filter; and
- an analysis circuit, operative to analyze crosstalk corrected signals and adjust the adjustable analog filter and the adjustable delay.

10 23. The signal processing system of Claim 22, wherein the analog filter comprises a tapped delay line filter.

24. The signal processing system of Claim 22, wherein the adjustable delay receives input from the analog filter.

15

25. The signal processing system of Claim 22, wherein the adjustable delay provides input to the analog filter.

26. The signal processing system of Claim 22, wherein the modeling filter
20 further comprises a fixed filter, coupled to at least one of the analog filter and the adjustable delay, operative to transmit frequencies above a threshold frequency and attenuate frequencies below the threshold frequency.

27. The signal processing system of Claim 22, wherein the analysis circuit
25 comprises a digital controller.

28. The signal processing system of Claim 22, wherein the analysis circuit is further operative to control transmission of signals having predetermined voltage patterns on the first and second communication channels.

30

29. The signal processing system of Claim 22, wherein the analysis circuit is further operative to calibrate the modeling filter.

30. An apparatus, for correcting crosstalk in a data communications system, comprising:

a crosstalk modeling filter that models a crosstalk response;

5 a difference node that subtracts the modeled crosstalk response from a communication signal, comprising the crosstalk, to yield a crosstalk-compensated signal, and

a control circuit that processes the crosstalk-compensated signal and adjusts the crosstalk modeling filter to reduce residual crosstalk in the crosstalk-compensated signal.

10

31. The invention of Claim 30, wherein the crosstalk modeling filter comprises an adjustable delay filter and an analog tapped delay line filter.

32. The invention of Claim 31, wherein the adjustable delay filter
15 circuit comprises a digital circuit that receives a data signal causing the crosstalk and amplitude limits the data signal.

33. The invention of Claim 31, wherein the crosstalk modeling filter
20 further comprises a first-order high-pass filter.

20

34. The invention of Claim 30, wherein the control circuit comprises an analog low-pass filter, an analog-to-digital converter, and a digital controller.

35. The invention of Claim 34, wherein the data communications
25 system communicates data at a baud rate and the analog-to-digital converter operates at a speed lower than the baud rate.

36. The invention of Claim 34, wherein the data communication
30 system transmits data at a baud rate and the digital controller operates at a speed less than baud rate.

37. The invention of Claim 34, wherein a device that measures signal magnitude based on signal amplitude provides input to the analog low-pass filter.

5 38. The invention of Claim 37, wherein the device comprises a power-detector.

39. The invention of Claim 37, wherein the device comprise a full-wave rectifier.

10

40. The invention of Claim 37, wherein the device comprises a half-wave rectifier.

41. The invention of Claim 37, wherein a spectral weighting filter
15 provides input to the device.

42. The invention of Claim 34, wherein the control circuit comprises a finite state machine.

20 43. The invention of Claim 34, wherein the control circuit comprises a microprocessor.

1/10

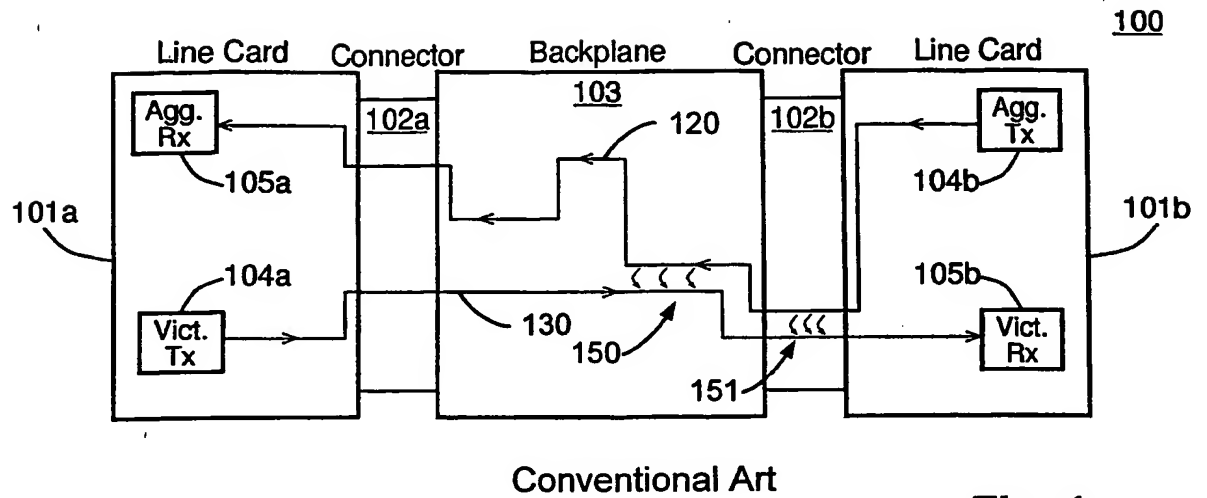
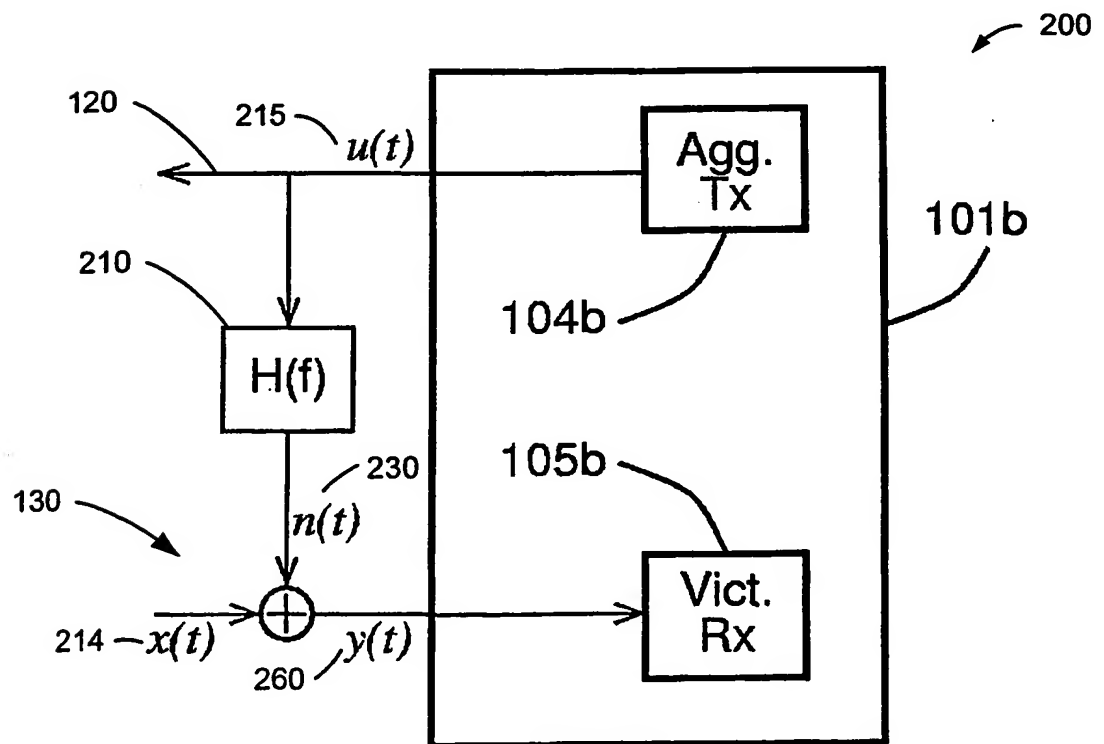


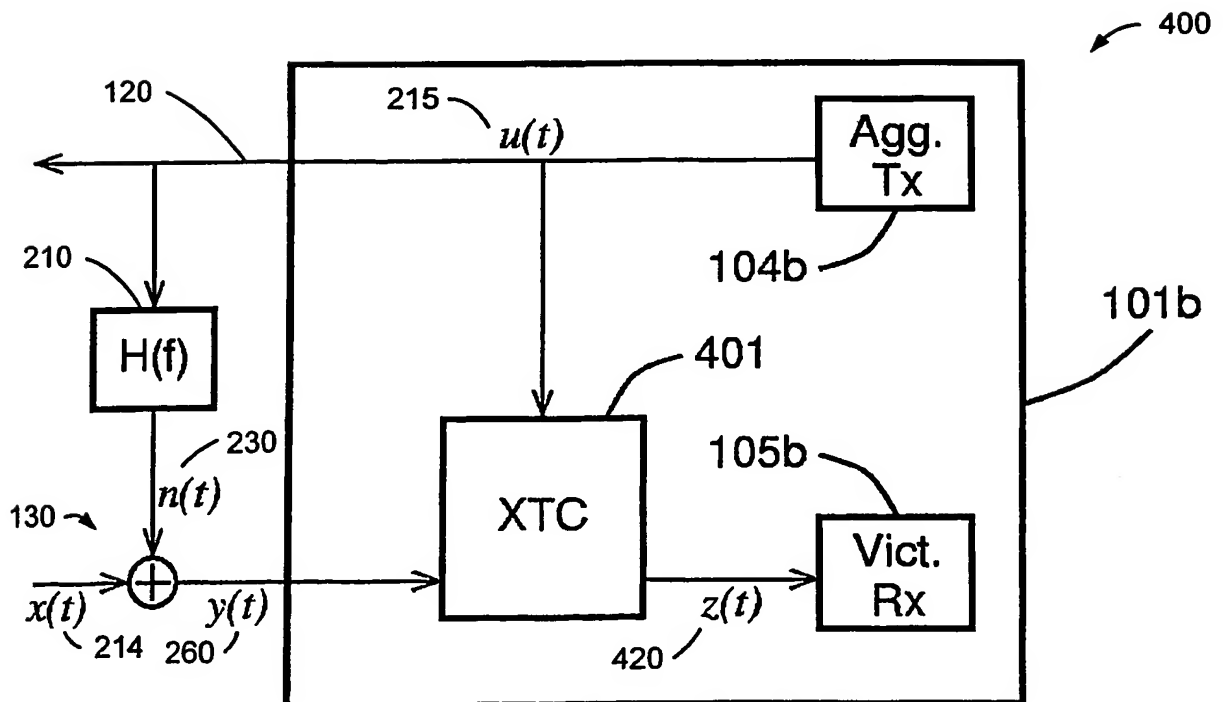
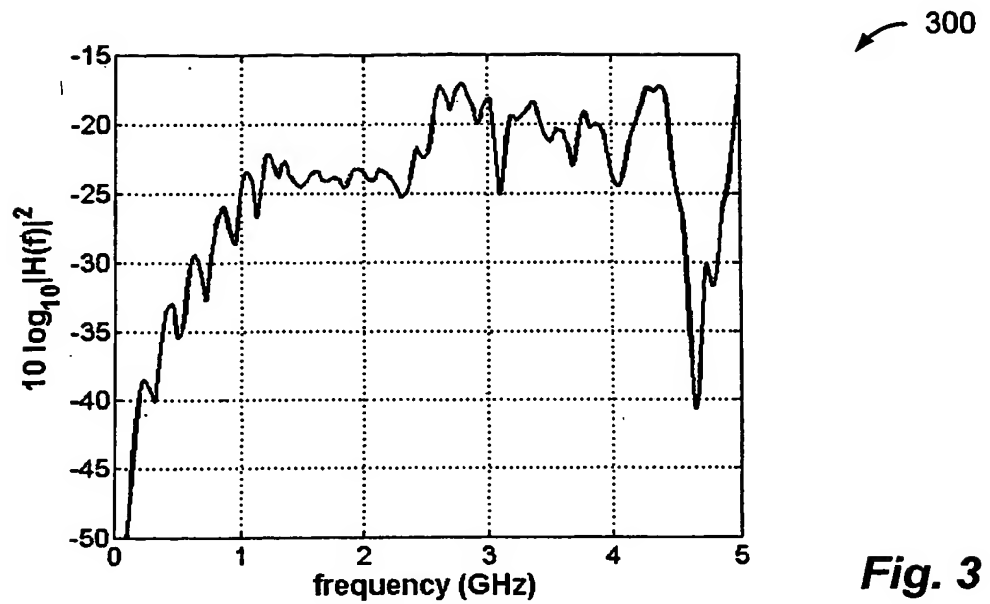
Fig. 1



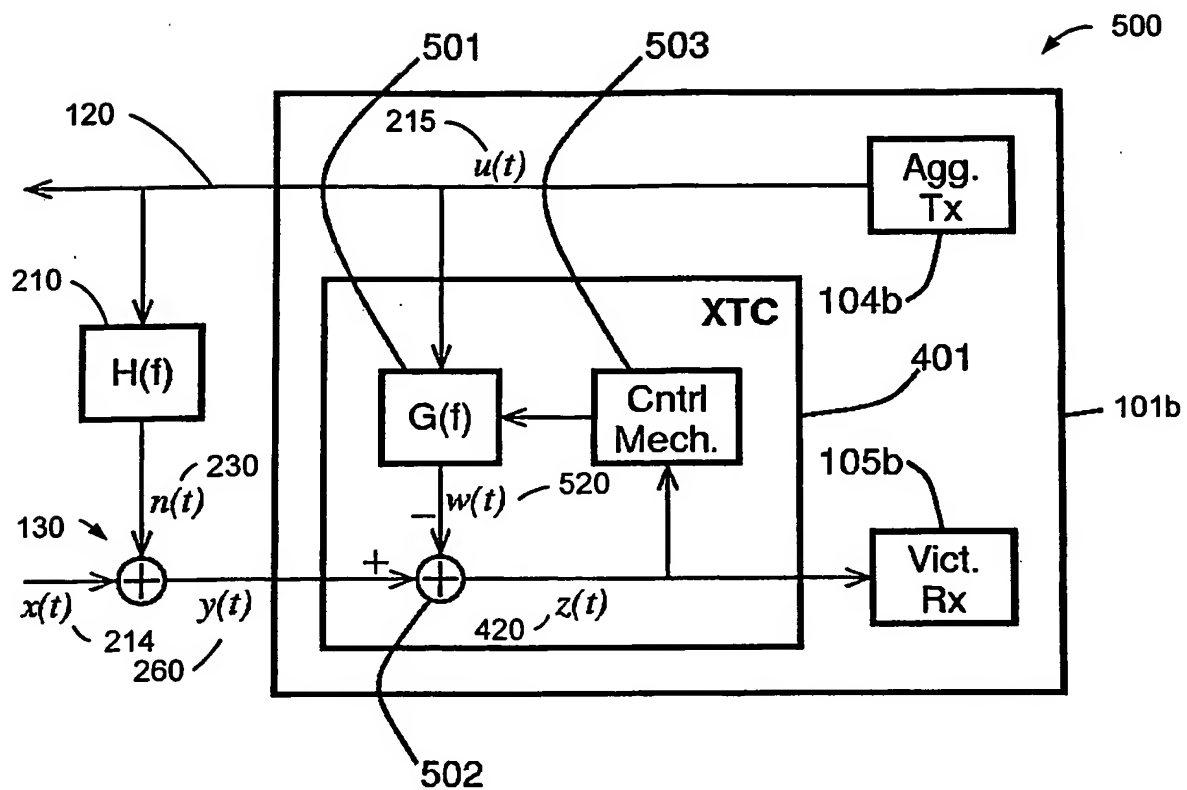
Conventional Art

Fig. 2

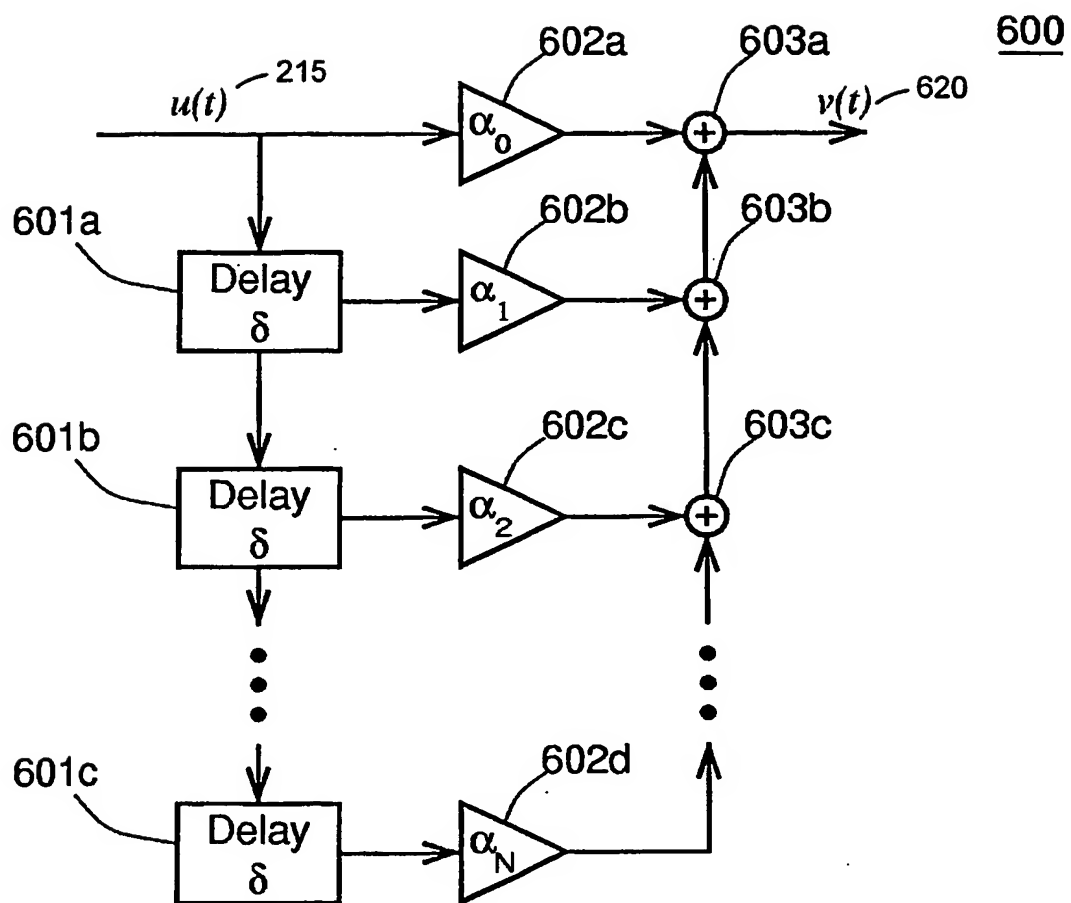
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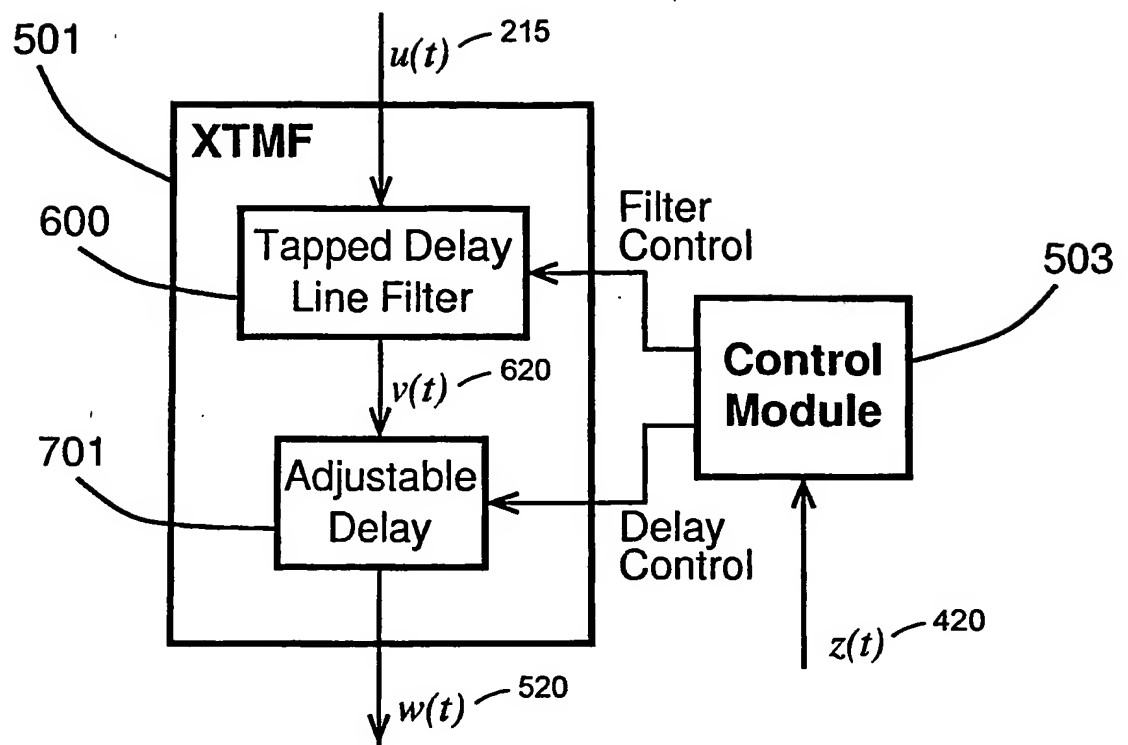
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**Fig. 5**

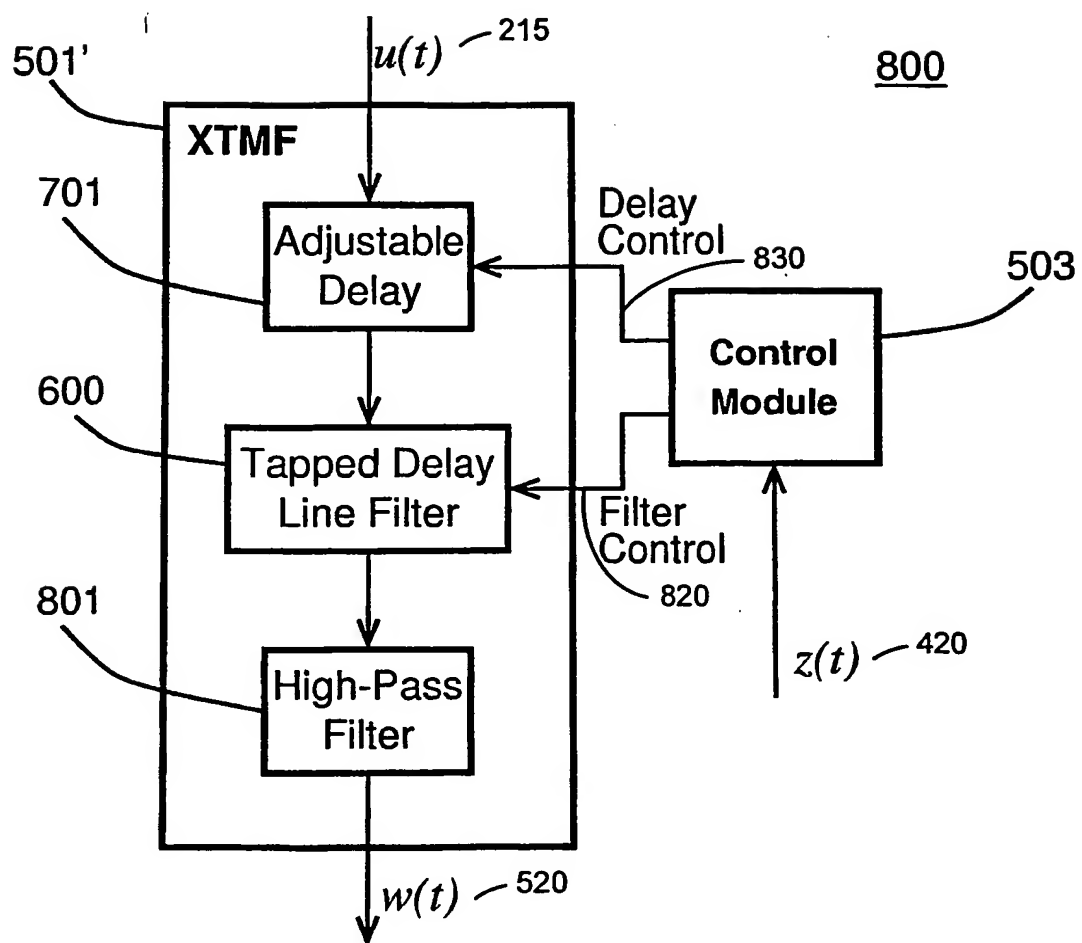
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**Fig. 6**

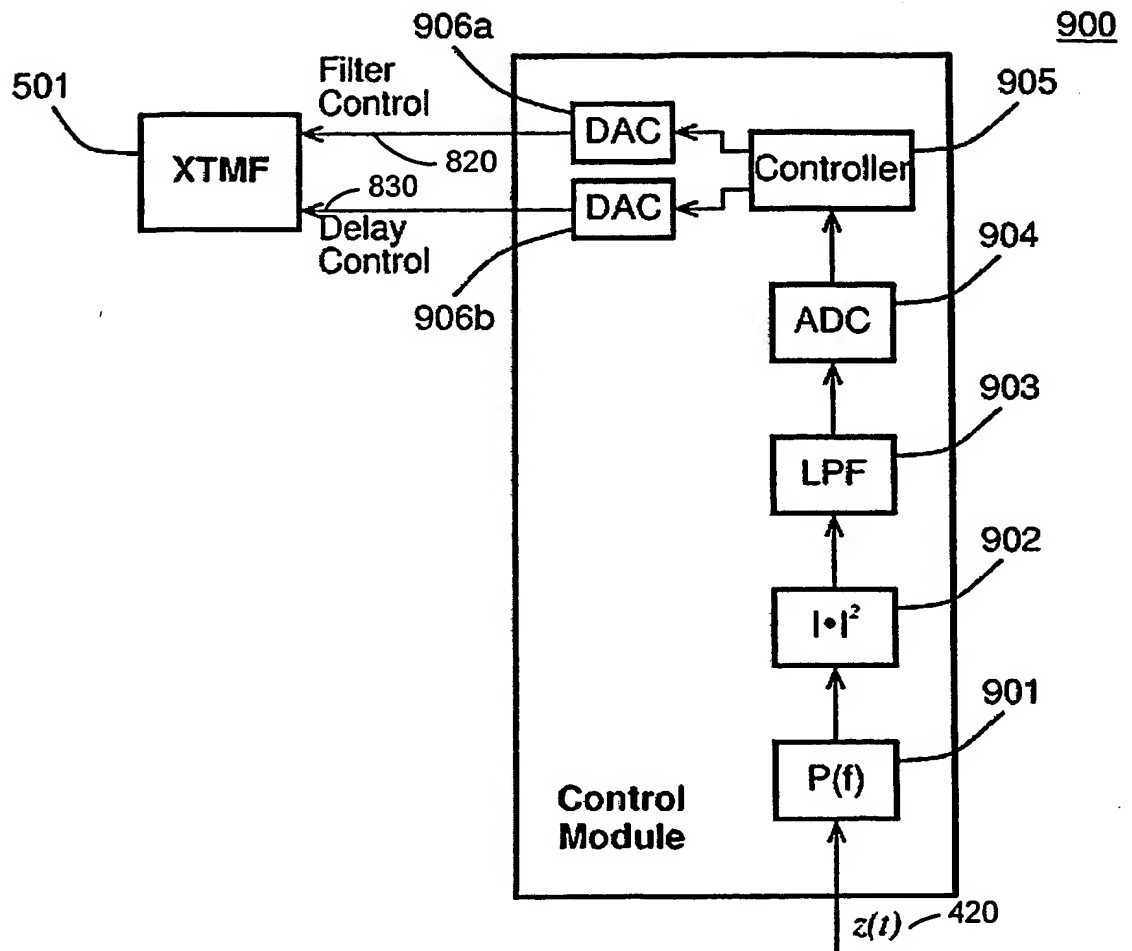
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**Fig. 7**

6/10

**Fig. 8**

7/10

**Fig. 9**

8/10

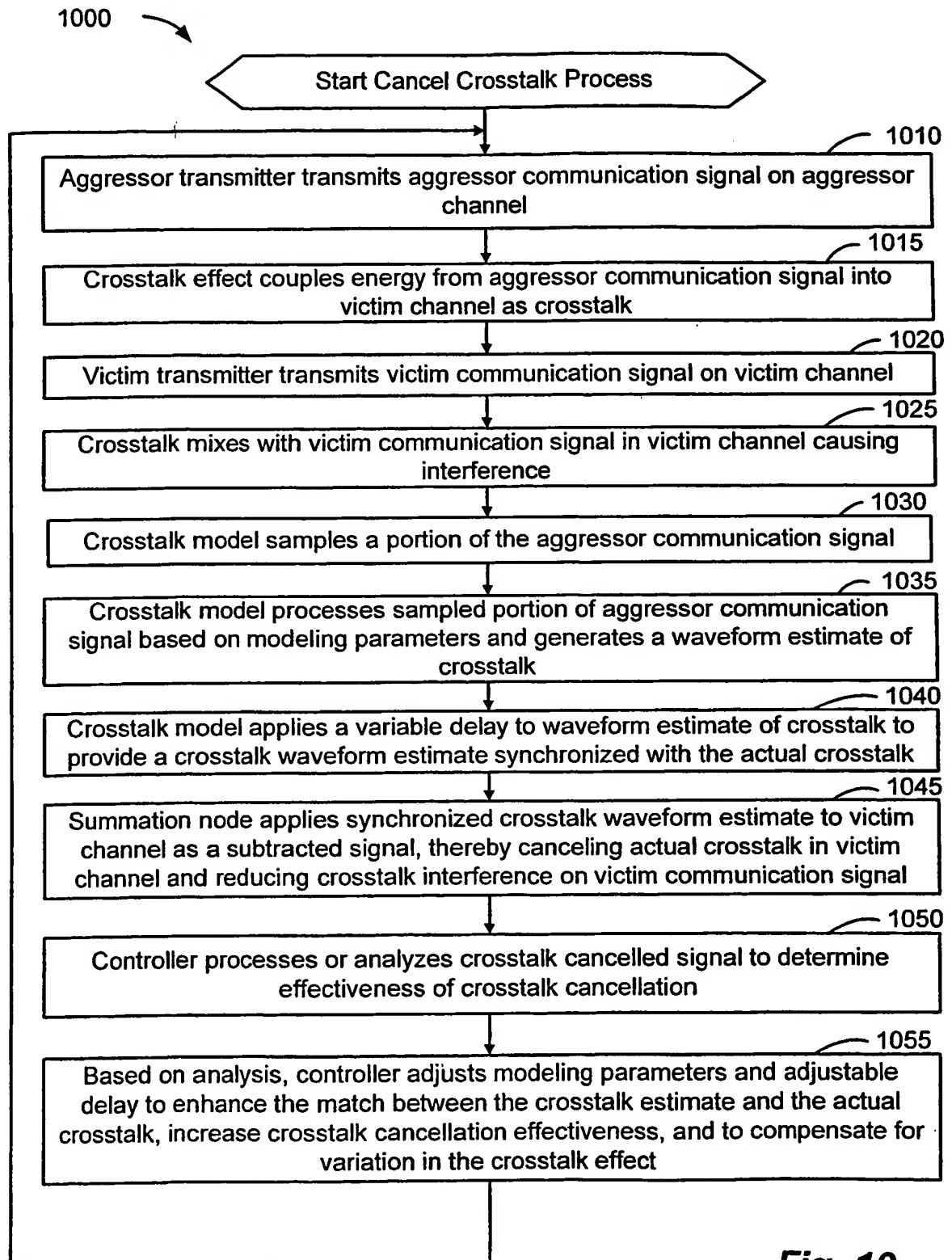


Fig. 10

9/10

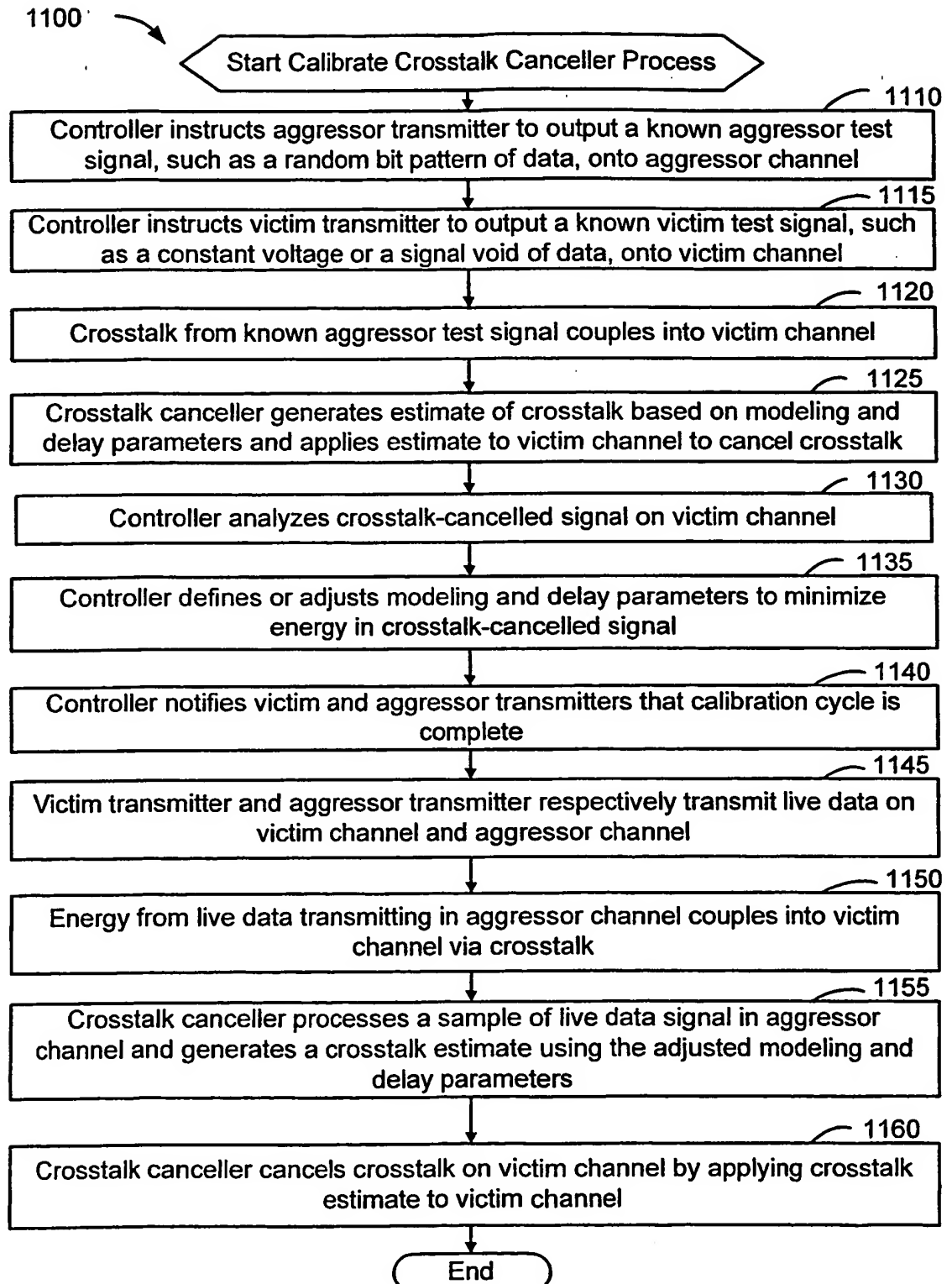


Fig. 11

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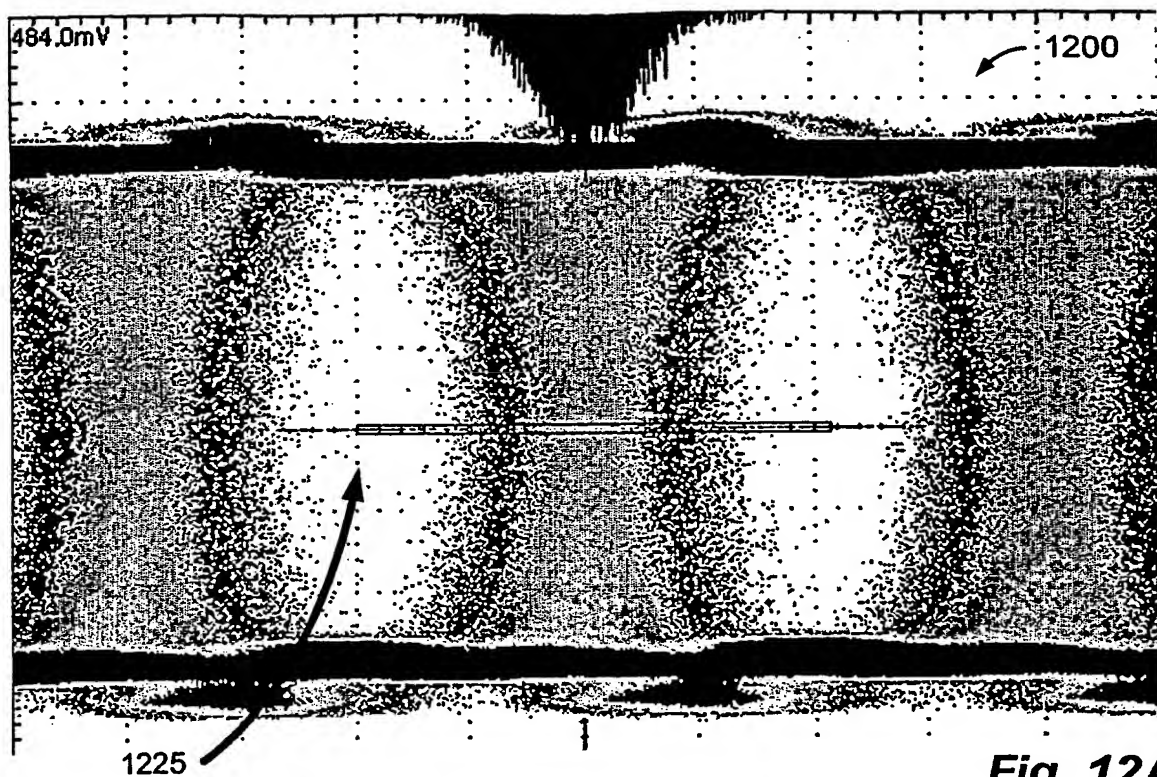


Fig. 12A

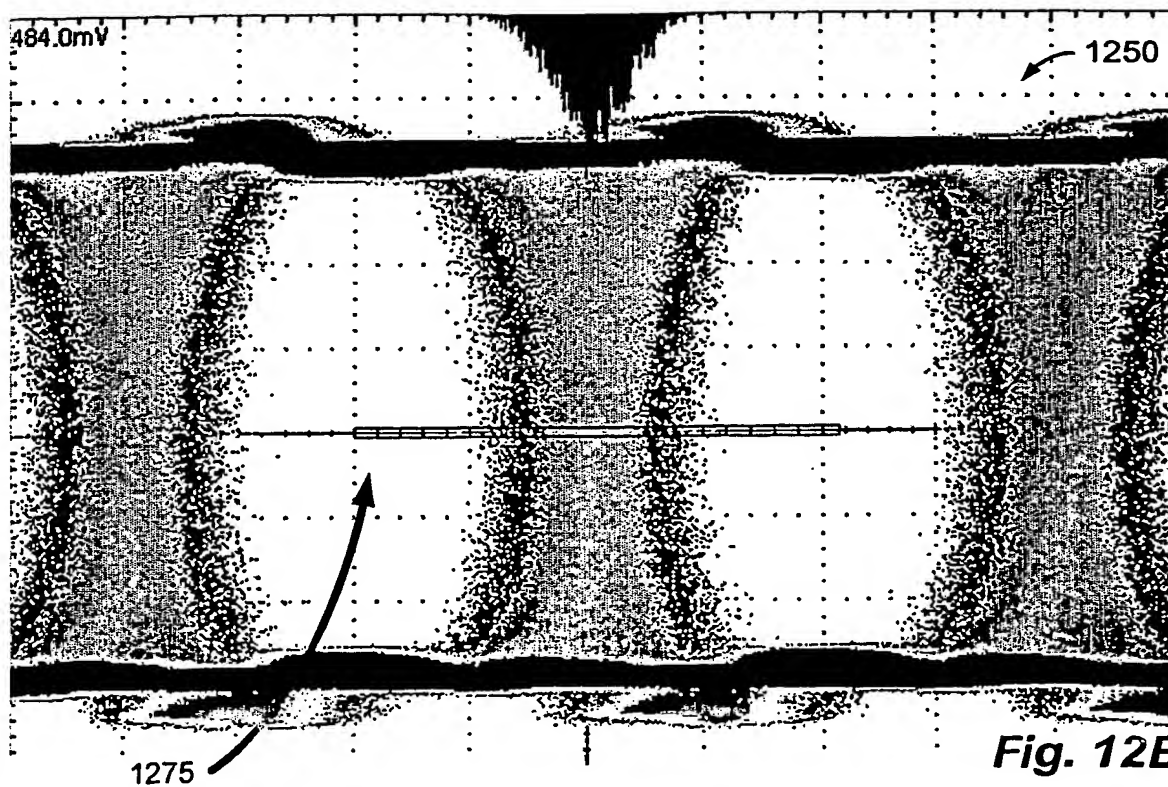


Fig. 12B